International Journal of Innovative Research in Computer and Communication Engineering (IJIRCCE)

(A Monthly, Peer Reviewed, Referred, Scholarly Indexed, Open Access Journal Since 2013)

IJIRCCE

Impact Factor 8.379

CERTIFICATE
OF PUBLICATION

The Board of IJIRCCE is hereby awarding this certificate to

ARASHANAPALLI GREESHMA

U.G. Student, Department of Computer Engineering, Stanley College of Engineering and Technology for Women, Hyderabad, India

in Recognition of Publication of the Research Paper Entitled

"FlavorForge – Crafting Detectable Recipes with RecipeNLG"

in IJIRCCE, Volume 12, Issue 6, June 2024

Google scholar · Crossref · Mendeley · doi · INNO SPACE SJIF Scientific Journal Impact Factor

e-ISSN: 2320-9801
p-ISSN: 2320-9798

ISSN INTERNATIONAL STANDARD SERIAL NUMBER INDIA

Editor-In-Chief

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Stanley College of Engineering and Technology for Women

Chapel Road, Abids, Hyderabad-500001

Department of AI&DS

Action Plan for Slow and Advanced Learners

Slow Learners

- Extra Remedial Classes for slow learners, conducted by each subject faculty
- Question Bank for each unit has been given based on previous year Question Paper.
- A spoken tutorial has been recommended.

Advanced Learners

- Moocs courses like NPTEL have been recommended for advanced learners
- Fast Learners are encouraged to participate in hackathons & Paper presentations etc.

Stanley College of Engineering and Technology for Women

Chapel Road, Abids, Hyderabad-500001

Department of AI&DS

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Stanley College of Engineering and Technology For Women
Department of Artificial Intelligence and Data Science
B.E – V - Sem Fast and Slow Learners A.Y-2023-24
Design and Analysis of algorithms

Sl. No	Fast Learners	Slow Learners
1	160621747005	160621747004
2	160621747008	160621747010
3	160621747011	160621747015
4	160621747014	160621747017
5	160621747020	160621747018
6	160621747024	160621747022
7	160621747025	160621747033
8	160621747028	160621747042
9	160621747029	160621747043
10	160621747030	160621747047
11	160621747036	160621747049
12	160621747037	160621747051
13	160621747040	160621747055
14	160621747041	160621747057
15	160621747044	160621747058
16	160621747050	160621747064
17	160621747052	160621747303
18	160621747053	160621747304
19	160621747059	160621747305
20	160621747063	160621747306

Analysis of Recursion Algorithm

Recursion is popular problem-solving approaches in data structure and algorithms.

problem solving approaches are totally based on recursion. Decrease and conquer, Divide and conquer, DFS traversal of tree and graph, Backtracking, Top-down approach of dynamic programming.

Time complexity of recursive algorithms in terms of input size.

Solve a problem by breaking it down into smaller sub problems. If the time complexity function of input size n is T(n), then the time complexity of the smaller sub problems will be defined by the same fn, but in terms of the subproblem's input size. An approach to write T(n) if we have k no. of subproblems:

T(n) = T(Input size of 1st subproblem) + T(Input size of 2nd subproblem) + --- + T(Input size of kth subproblem) + Time complexity of additional operations other than recursive calls.

Ex- Finding the nthe Fiboracci — 2 subproblems of size (n-1) and (n-2)

fib(n) = fib(n-1) + fib(n-2)

Base case: if (n<=1), return n

when have 2 base cases: fib(0) = 0 fib(1) = 1.

Time Complexity T(n) = Time Complexity of finding the (n-1)th Fibonacci + Time Complexity of finding the (n-2)th fibonacci + Time complexity of addition operation.

$= T(n-1) + T(n-2) + O(1)$

$= T(n-1) + T(n-2) + c$ where $T(n) = c$ for $n \le 1$.

Stanley College of Engineering and Technology for Women

Chapel Road,Abids,Hyderabad-500001

Department of Computer Engineering

Action Plan for Slow and Advanced Learners

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- Spoken tutorial has recommended.

Advanced Learners

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- Fast Learners are encouraged to participate in hackthons & Paper presentations

STANLEY COLLEGE OF ENGINEERING AND TECHNOLOGY FOR WOMEN

DEPARTMENT OF COMPUTER ENGINEERING

OOPS USING JAVA

Slow Learners

S.No	Roll No	Name of the Candidate	Marks
1	160622740011	DAYALA NAGA PRASANNA	20
2	160622740015	JADHAV RUPALI	22
3	160622740016	K. SPANDANA	0
4	160622740026	MARAM BHAVYA	21
5	160622740031	MUTHIREDDY VEERA VENKATA VARSHI	20
6	160622740036	NUKALA HARINI	21
7	160622740039	PENUGONDA PAVANI	21
8	160622740046	SALUVADI LUCKSHME VINAYA SAMHITH	21
9	160622740048	SHANKER KUNDANIKA	21

STANLEY COLLEGE OF ENGINEERING AND TECHNOLOGY FOR WOMEN

DEPARTMENT OF COMPUTER ENGINEERING

OOPS USING JAVA

Advanced Learners

S.No	Roll No	Name of the Candidate	Marks
2	160622740008	C ARSHITA	40
3	160622740013	ERANKI LAKSHMI MRUDULA	39
4	160622740018	KANIZ E FATIMA	39
5	160622740023	MAHITA LINGAMPALLY	38
6	160622740024	MAHIYA AFREEN	38
7	160622740032	N APOORVA	39
8	160622740035	NAREDLA SATHWIKA VARMA	37
9	160622740041	PERUMANDLA VINUTHNA	37
10	160622740042	R. VISHISTA	39
11	160622740043	RASURI PAVITHRA	37
12	160622740045	REVURI RAGAMAYEE	38
13	160622740051	SOFIA SAMREEN	39
14	160622740054	SUMAEYYA ABDUL KHALID	39
15	160622740057	TANK LAKSHMINANDINI	39
16	160622740058	THULLURI SRIVALLI UMA	40

Samia Tazeen
1606-22-740047
Mod 1 ASSIGNMENT

Part A

1.) Define Garbage collection.
Garbage collection in Java is the automated process of deleting code that's no longer needed for or used. This automatically frees up memory space and ideally makes coding Java apps easier for developers.

2) what are packages? list the built in packages.
A Java package is a group of similar types of classes, interfaces and sub packages. Packages help in avoiding name conflicts. provides a way to encapsulate related classes.

→ Java.lang :- This package automatically imported into every Java program.

→ Java.util :- This package contains utility classes, data structures, date & time related classes.

→ Java.io :- This package provides classes for input & output operations. It includes classes for working with files.

→ Java.net :- This package contains classes for networking operations.

3) what is widening and narrowing
widening & narrowing refer to type casting in Java, specifically dealing with primitive data types. These conversions are essential when assigning values or performing operations involving different data types.

This is to certify that

Akula Tharuni

successfully completed and received a passing grade in

SQL and Relational Databases 101

(DB0101EN, provided by IBM)

A course on skillsbuild.skillsnetwork.site
Powered by IBM Developer Skills Network.

Issued by
IBM SkillsBuild

November 22, 2023

Authenticity of this certificate can be validated by going to:
https://courses.skillsbuild.skillsnetwork.site/certificates/4a2a9d75dc8f4fef8face951864fde2c

Certificate ID Number: **4a2a9d75dc8f4fef8face951864fde2c** **November 22, 2023**

Stanley College of Engineering & Technology for Women
Department of CSE

MENTORS CIRCULAR

This is to inform the following faculty have been allocated as mentees for next three years

S.No	Name of the faculty	Class	Roll No	Signature
1	Mrs.P.Veena Kumari	III SEM CSE A	160623733001 to 160623733025	
2	Ms.Hafsa IUA		160623733026 To 160623733048	
3	Dr.P.R.Anisha		160623733049 To 160623733306	
4	Mrs.P.Rishitha	III SEM CSE B	160623733065 To 160623733087	
5	Dr.Shivani Yadao		160623733088 To 160623733110	
6	Mrs.Sarah Angilina		160623733111 To 160623733312	
7	Mrs.M.Swathi Sree	III SEM CSE C	160623733128 To 160623733151	
8	Dr.D.Radhika		160623733152 To 160623733174	

9	~~Mrs.N.Ruga Chandrika~~ B. Soujanya		160623733175 To 160623733318	
10	Mrs.B.Manisha	V SEM CSE A	160622733001 to 160622733023	
11	Mrs.V.Anitha		160622733024 to 160622733046	V. A
12	Mrs.KSrilatha		160622733047 to 160622733307	
13	Ms.Shaheda Begum	V SEM CSE B	160622733064 to 160622733085	Shah
14	Mrs.Sumayya Afreen		160622733086 to 160622733109	
15	Mrs.M.Sowmya		160622733110 to 160622733314	Som
16	Mrs.Ghousia Begum/NAZIA TABASUM	V SEM C	160622733126 to 160622733149	Ghousi
17	Mrs.Bhanu Priya		160622733150 to 160622733173	
18	Dr.M.Swapna		160622733174 to 160622733321	Swapna
19	Mrs.KSrilatha	VII SEM CSE A	160621733001 to 160621733024	
20	Mrs.V.Anitha		160621733025 to 160621733049	V. A

Mrs.M.Tejaswee Reddy		160621733050 to 160621733308	
Mrs.Shuguftа Fathima	VII SEM CSE B	160621733061 to 160621733081	
Mrs.A.Tejaswee		160621733082 to 160621733103	
Mrs.T.Monika Singh		160621733104 to 160621733310	
Mrs.M.Swathi Sree	VIISEM CSE C	160621733121 to 160621733142	
Dr.D.Radhika		160621733143 to 160621733165	
Mrs.N.Raga Chandrika		160621733166 to 160621733365	

HOD

Stanley College of Engineering & Technology for Women

Department of CSE

B.E. V Sem list of slow learners

S. No.	Roll No.	NAME	MID 1	MID 2
		ETC		
			25	25
1	160621733004	A. Pragna	9	12
2	160621733023	Aashritha Reddy	12	13
3	160621733041	Likhitha	7	11
4	160621733045	Mamatha	3	0
5	160621733056	Rania Mahek	13	6
6	160621733058	Aishwarya	11	13
7	160621733302	T Keerthi	12	14
		ALC		
1	160621733002	Harshita	12	14
2	160621733023	Aashritha Reddy	14	12
3	160621733024	K. Rishitha	14	13
4	160621733025	K. Rithika	8	12
5	160621733036	Bindhusha Goud	14	3
6	160621733046	P.Yashashwini Reddy	7	6
7	160621733056	Rania Mahek	2	5
8	160621733060	Ananya Reddy	5	14
		DBMS		
1	160621733004	A. Pragna	11	14
2	160621733023	Aashritha Reddy	3	13
3	160621733033	Bhavani	0	12
4	160621733036	Bindhusha Goud	0	13
5	160621733045	Mamatha	11	13
6	160621733056	Rania Mahek	5	7
7	160621733058	Aishwarya	14	13
8	160621733060	Ananya Reddy	6	13
9	160621733061	Architha	11	11
10	160621733306	huda tariq	0	13

Class Incharge:

HOD

Stanley College of Engineering & Technology for Women
Department of CSE
B.E. V Sem list of fast learners

Sl. No	Roll No.	Name	MID-1	MID-2
1	160621733001	Lydia Lois	24	25
3	160621733003	Mallika	22	21
5	160621733005	Tejasree	23	22
6	160621733006	Asma	25	25
7	160621733007	Ayesha	22	19
8	160621733008	Ayushi	24	23
9	160621733009	B. Komal Singh	20	20
10	160621733010	B Nayantara Swaraj	19	21
11	160621733011	Keerthi	21	22
12	160621733012	Maneesha	20	15
13	160621733013	Pranjali	22	23
14	160621733014	Manichandana	19	22
15	160621733015	Sindhu	23	21
16	160621733016	Keerthi Reddy	20	19
17	160621733017	G.S.C.Sathwica	22	19
18	160621733018	Harini	22	25
19	160621733019	Shailaja	23	20
21	160621733021	Akshaya	20	20
22	160621733022	Nandini	24	25
24	160621733024	K. Rishitha	22	23
25	160621733025	K. Rithika	22	24
26	160621733026	Vaishnavi	24	22
27	160621733027	Keerthi	22	19
28	160621733028	Keerthi Sai	22	20
29	160621733029	Sritisha	23	21
30	160621733030	K. Swetha	20	22
31	160621733031	K. Meghana	24	24
32	160621733032	Laxmi Prasanna	21	20
34	160621733034	Vaishnavi	22	21
36	160621733036	Bindhusha Goud	24	25
38	160621733037	M.Samhitha	21	20
39	160621733038	Tejasree	20	21
40	160621733039	Smarani	24	19
41	160621733040	Rithika Reddy	24	23
43	160621733042	N. Ranvitha	20	24
44	160621733044	Nifa	19	19
46	160621733046	P.Yashashwini Reddy	24	25
47	160621733047	Soni	25	25
48	160621733048	Lahari	20	20
49	160621733049	Salma	23	21

50	160621733050	Sana	24	25
51	160621733051	Teheseen Mehek	23	22
52	160621733052	Shreeya	23	20
53	160621733053	Sofiya	19	23
54	160621733054	S.Akhila	19	20
57	160621733057	Baby Vijaya Shree	23	22
59	160621733059	Deekshitha	25	25
60	160621733060	Ananya Reddy	22	19
61	160621733061	Architha	24	23
62	160621733062	V N Devi Mani Priya	20	20
63	160621733063	Sruthi	19	21
64	160621733064	Manila	21	22
65	160621733065	Amrutha Sriya	23	22
66	160621733301	Datta Priya	23	20
67	160621733303	A Vineetha	19	23
68	160621733304	B Varshitha	19	20
69	160621733305	k sirichandana	23	22
70	160621733306	huda tariq	25	25
71	160621733307	T Anitha	22	19

Class Incharge:

HOD

Elite

NPTEL Online Certification

(Funded by the MoE, Govt. of India)

This certificate is awarded to

SANJANA GAVADA

for successfully completing the course

Programming In Java

with a consolidated score of **72** %

Online Assignments	24.94/25	Proctored Exam	47.25/75

Total number of candidates certified in this course: **9357**

Jan-Apr 2023

(12 week course)

Prof. Debjani Chakraborty
Coordinator, NPTEL
IIT Kharagpur

Indian Institute of Technology Kharagpur

FREE ONLINE EDUCATION swayam शिक्षित भारत, उन्नत भारत

Roll No: NPTEL23CS49S53754164

To validate the certificate

No. of credits recommended: 3 or 4

Roll No: 160621733107

Stanley College of Engineering & Technology For Women
(Autonomous)

Department of CSE

Date: 22/Mar/2024

CSE VI 'B'

Slow learners After Midl

*Score below 15 marks in midl

Roll No	Midl	Quiz	Student Signature	Mentor Sign. Name + Sign.
160621733094	6.5	4.5	Bhaskita	Tejaswee mam
096	14.5	3	Anuraga	Tejaswee Mam
102	12	1	[illegible]	
107	8.5	2.5	Srichandana	Tejaswee mam
109	13	2		Mrs Tejaswee.
115	7.5	2		Mr Monika Singh
123	14	2.5	Jyothsna.	→ Mona Singh mam
127	5	1		
128	10	3		
129	12.5	2	[illegible]	mona singh Mam

Action Plan:

1. Information to Mentor Sent.
2. Speaking, motivating Student to do well.
3. Task given to students :- Answer the Midl Question Paper without choice + Submit by 25th March 2024.

22/Mar/24

Dr D Radhika
Faculty Member

HoD.

* Photo of this sheet sent to Mentors, Class Incharge.

Sec B Mentors

Mrs Shugutha (66-89)

Mrs A. Tejaswi (90-112)

Mrs J. Monika Singh (113-130, 308, 313)

Class Incharge:-
Mrs Chandrika.

Stanley College of Engineering & Technology for Women (A)

Department of CSE

Date: 29/June/24.

CSE VI Sem Sec B

SE Slow learners List Based on Mid2 <15

Roll No	Mid2 marks	
74	14	
76	13	1
83	13	
94	11	
99	13	
100	7	2
107	6	
115	7	
127	13	
128	14	
129	7	3
310	09	

Students orally briefed about their Performance and suggested to improve their Learning & Writing skill.

Mentors are informed & requested to motivate the students.

1. Mrs Shuguftha (66-89)
2. Mrs A. Tejaswi (90-112)
3. Mrs T. Monikasingh (113-130, 308, 313).

Observation & suggestion to students for improving score:-

1. Key Points to be learned correctly & write them. (Improve Remembrance).
2. Drawing diagrams & writing case based examples will give good marks.

Dr D Radhika

Assig

Stanley College of Engineering & Technology for Women(A)

Chapel Road, Abids, Hyderabad.

Department of Electronics and Communications Enginering

List of Slow Learners

Section 1

Sl.No	Roll No
1	160622735001
2	160622735002
3	160622735006
4	160622735017
5	160622735019
6	160622735031
7	160622735033
8	160622735044
9	160622735048
10	160622735055

Section 2

Sl.No	Roll No
1	160622735081
2	160622735103
3	160622735109
4	160622735115
5	160622735117
6	160622735121
7	160622735124
8	160622735301
9	160622735307

List of Fast Learners

Section 1

Sl.No	Roll No
1	160622735003
2	160622735016
3	160622735021
4	160622735028
5	160622735030
6	160622735037
7	160622735040
8	160622735041
9	160622735047
10	160622735052
11	160622735053
12	160622735061
13	160622735070
14	160622735071

Section 2

Sl.No	Roll No
1	160622735070
2	160622735071
3	160622735076
4	160622735077
5	160622735087
6	160622735091
7	160622735092
8	160622735094
9	160622735100
10	160622735101
11	160622735102
12	160622735110
13	160622735112
14	160622735113
15	160622735114
16	160622735116
17	160622735305
18	160622735306
19	160622735313

Class Incharges

Mrs. T.Prasanna(SEC-1)

Mrs.G.Nirmala(SEC-2) 17/1/2024

HOD 17/1/24

STANLEY COLLEGE OF ENGINEERING AND TECHNOLOGY FOR WOMEN
(AUTONOMOUS)
Hyderabad – 500 001
(Affiliated to Osmania University & Approved by AICTE)
(All eligible UG Courses are accredited by NBA & Accredited by NAAC with 'A' Grade)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Remedial Class Attendance for slow learners (III SEM ECE) A.Y: 2023-2024

Name of the Course: Electronic Devices & Circuits Course Code: SPC 301 EC

Name of the Faculty Member: T. Prabhanna

Topic Covered: h-parameter model, MOSFET

S.No.	Roll Number	Student Name	Month	Jan	Jan			
			Date	17/1/24	24/1/24			
			Time					
				1	2	3	4	5
1	160622735002	A. Vaishnavi		Vaishnavi	Vaishnavi			
2	160622735006	A. Geetha.			A Geetha			
3	160622735017	D. Shreya			Shreya			
4	160622735019	Anusha			Anusha			
5	160622735031	Vaishnavi			Vaishnavi			
6	160622735033	[illegible]			[illegible]			
7	160622735044	M. Likhitha.			[illegible]			
8	160622735048	~~[illegible]~~			← " →			
9	160622735055	P. Kavya.			Kavya			
10	160622735081	C. Akshitha			Akshitha			
11	160622735103	← Ab →			← " →			
12	160622735109	N. Suaakshitha			[illegible]			
13	160622735117	← Ab →			← " →			
14	160622735301	A. Navya			Navya			
15	160622735307	K. Saivyshravi			Vyshu			
16	160622735001	Samena Rafeeq			Samena			
17	115	UMA RANI			[illegible]			
18	121	← Ab →			← Ab →			
19	124	S. Deepika			Deepika			
20								
21								
22								
23								
24								
25								
26								
27								
28								
29								
30								
Total number of students attended								
Faculty Member Signature with Date					24/1/24			

Class In charge

24/1

HOD,ECE

24/10/24

CERTIFICATE OF PARTICIPATION

THIS CERTIFICATE IS PRESENTED TO KELOTH CHAITAYA LAKSHMI

FROM THE DEPARTMENT OF ECE (STANLEY COLLEGE)

In recognition of his/her efforts in completing the Two-Day National Workshop on **Explorig the Art of PCB DESIGNING with HANDS ON** Jointly organized by UCE, OU and CBIT in association with IETE

Conducted On 7th and 8th May,2024

SRI. T SRIDHER Faculty Coordinator, IETE-ISF, CBIT	**DR. K. VASANTH** Head of Departement, ECE,CBIT	**DR. D. RAMA KRISHNA** Head of Department, ECE,OU	**PROF. C. V. NARASIMHULU** PRINCIPAL,CBIT

[ecsa-11] sciforum-096322 - files accepted

From: info@sciforum.net

To: gunthakarthik@rocketmail.com

Cc: vaishuvnps@gmail.com; shashritha7189@gmail.com; anjalinarri@gmail.com

Date: Wednesday, October 16, 2024 at 07:22 PM GMT+5:30

ECSA-11 2024 Conference

The 11th International Electronic Conference on Sensors and Applications

26–28 November 2024 | Online

MDPI

Dear Mr. Karthik,

Your submission has been approved by our editorial team.

Submission ID: sciforum-096322
Title: AGRICULTURAL CROP YIELD PREDICTION USING ADVANCED DATA ANALYSIS TECHNIQUES – CASE STUDY
Author: Guntha Karthik, B Vaishnavi, N Shashritha, N Anjali
Event: 11th International Electronic Conference on Sensors and Applications
Section: Smart Agriculture Sensors

https://sciforum.net/dashboard/author/submissions/ff05d5965922e88a1a48fe9efe9289d6

Kind regards,
Your ecsa-11 Organizing Team
ecsa@mdpi.com

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engineering proceedings

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Agricultural Crop Yield Prediction using Advanced Data Analysis Techniques – Case Study

Dr Guntha Karthik[1], B. Vaishnavi, N. Shashritha, N. Anjali

[1] Department of Electronics and communication, Associate Professor, Stanley College of Engineering and Technology for Women (Autonomous), Abids, Hyderabad. 500001.

[2] B.E. Students, ECE Department, Stanley College of Engineering and Technology for Women (Autonomous), Hyderabad, Telangana, India. vaishavirpo@gmail.com, shashritha7189@gmail.com, anjalinarri@gmail.com

Correspondence: gunthakarthik@rocketmail.com; Tel.: (91-9052611677/8096056466)

Abstract: Agricultural crop yield prediction is crucial for enhancing food security, optimizing resource use, and ensuring sustainable agricultural practices. This project focuses on enhancing food security and sustainable agricultural practices by predicting crop yields using machine learning techniques. By integrating data from weather patterns, soil properties, and historical crop performance, the study aims to aid farmers and policymakers in decision-making. The research examines the correlation between crop yield and environmental factors such as nitrogen, phosphorus, potassium, rainfall, temperature, and fertilizer application. Multilinear Regression (MLR), Radial Basis Function (RBF), and Support Vector Machine (SVM) models are applied to predict yields, with SVM achieving the highest accuracy at 92.03%, followed by MLR at 88.56% and RBF at 75.36%. The data collection for this study includes nutrient levels in the soil, historical weather patterns, and fertilizer usage from the Peddapalli district, Telangana, India. MLR identifies linear relationships, RBF captures non-linear patterns, and SVM handles high-dimensional data to enhance prediction accuracy. The results indicate that while MLR and RBF provide valuable insights, SVM is the most robust tool for forecasting crop yields. This research holds significant potential for improving agricultural productivity and resource management, offering farmers crucial insights for better planning and allocation of resources.

Keywords: Agricultural Crop Prediction, Machine Learning, Multilinear Regression, Radial Basis Function, Precision Agriculture, Environmental Factors, Crop Yield Forecasting.

1. Introduction

Machine learning is a valuable decision-making tool for predicting agricultural yields and deciding the type of crops to sow and things to do during the crop growing season. To aid crop prediction studies, several machine learning methods have been used. Machine learning techniques are utilized in various sectors, from evaluating customer behaviour in supermarkets to predicting customer phone usage. For some years, agriculture has been using machine learning techniques. Crop prediction is one of agriculture's complex challenges, and several models have been developed and proven so far. Because crop production is affected by many factors such as atmospheric conditions, type of fertilizer, soil, and seed, this challenge necessitates using several datasets[1-4]. This implies that predicting agricultural productivity is not a straightforward process; rather, it entails a series of complicated procedures. Crop yield prediction methods can now reasonably approximate the actual yield, although more excellent yield prediction performance is still desired. Agriculture, as the cornerstone of human civilization, faces unprecedented challenges in the 21st century due to factors such as climate change, population growth, and resource scarcity. To address these challenges, modern technologies, particularly data-driven approaches, are increasingly becoming integral to sustainable and efficient agricultural practices.

Citation: To be added by editorial staff during production.

Academic Editor: Firstname Lastname

Published: date

STANLEY COLLEGE OF ENGINEERING AND TECHNOLOGY FOR WOMEN
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(All eligible UG Courses are accredited by NBA & Accredited by NAAC with 'A' Grade)

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Remedial Class Attendance for III SEM ECE A.Y: 2023-2024

Name of the Course: EMTL
Course Code:
Name of the Faculty Member: Dr. K. N. Sahu
Topic Covered:

S.No.	Roll Number	Student Name	Month		Jan	Jan	Jan	
			Date		9-1-24	19-1-24	20-1-24	
			Time		2-30 - 4.30 P.M.	2.30 - 4.30 P.M.	12.30 - 2.30 pm	
				1	2	3	4	5
1	160622735301	B. Pallavi			B. Pallavi	B. Pallavi	B. Pallavi	
2	160622735115	R. Umamani			[illegible]	–	–	
3	160622735309	P. BHOOMIKA			Bhoomika	Bhoomika	Bhoomika	
4	160622735301	A. Navya			Navya	Navya	Navya	
5	160622735074	A. Meghana			A. [illegible]	–	–	
6	160622735080	Ch. Navitha			Navitha	–	–	
7	160622735106	M. Nayana			M. Nayana	–	–	
8	160622735075	A. Harshitha			[illegible]	–	–	
9	160622735110	D. Navya			[illegible]	–	–	
10	160622735114	P. Prithvika			[illegible]	–	–	
11	160622735088	G. Hima bindhu			[illegible]	–	–	
12	160622735018	D. Sripriya			D. Sripriya	D. Sripriya	D. Sripriya	
13	160622735038	D. Supriya			D. Supriya	D. Supriya	D. Supriya	
14	160622735076	A. Vaishnavi			Vaishnavi	–	–	
15	160622735312	Shahista			[illegible]	–	–	
16	160622735310	Rajeswari			[illegible]	Rajeswari	Rajeswari	
17	160622735308	Pooja			[illegible]	Pooja	[illegible]	
18	160622735314	V. Vaishnavi			[illegible]	Vaishnavi	Vaishnavi	
19	160622735302	Poojitha			Poojitha	–	Poojitha	
20	160622735103	M. Ritika			[illegible]	–	–	
21	160622735113	P. Ananya Sri			P. Ananyasri		–	
22	160622735079	Ch. Harichandana			[illegible]	[illegible]	–	
23	160622735054	P. Hasini			–	P. Hasini	–	
24	160622735313	V. Sreeja			–	Sreeja	–	
25	160622735310	P. Rajeshwari			–	–	P. Rajeshwari	
26	160622735308	K. Pooja			–	–	[illegible]	
27								
28								
29								
30							"	
Total number of students attended								
Faculty Member Signature with Date								

20/1/24

HOD,ECE

Class In charge

20/1/24

STANLEY COLLEGE OF ENGINEERING AND TECHNOLOGY FOR WOMEN
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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Remedial Class Attendance for slow learners (III SEM ECE) A.Y: 2023-2024

Name of the Course: EMTL Course Code:
Name of the Faculty Member: Dr. K.N. Sahu
Topic Covered:

S.No.	Roll Number	Student Name	Month		Jan	Jan	Jan	
			Date		9-1-24	19-1-24	20-1-24	
			Time		2.30-4.30 PM	2.30-4.30 PM		
				1	2	3	4	5
1	160622735002	Vaishnavi			Vaishnavi	vaishnavi	Vaishnavi	vaishnavi
2	160622735006	A. Geetha.			A Geetha	–	–	
3	160622735017	Dshreya			D.shreya	–	–	
4	160622735019	D. Anusha			Anusha	Anusha	Anusha	
5	160622735031	A. Navya			Navya	Navya	–	
6	160622735033					–	–	
7	160622735044	M. Likhitha			Likhitha	–	–	
8	160622735048	M. Chaitanya sree			M.chaitanyasree	M Chaitanyasree	Chaitanya sree	
9	160622735055	P. Kavya			Kavya	Kavya	Kavya	
10	160622735081	C. Akshetha			Akshetha	Akshetha	Akshetha	
11	160622735103	M. Ritika			M. Ritika	–	–	
12	160622735109	N. Surakshitha			[illegible]	[illegible]	[illegible]	
13	160622735117	R. Pranathi			R. [illegible]	R. [illegible]	–	
14	160622735301	A. Navya			Navya	Navya	Navya	
15	160622735307	K. Sai vyshnavi			K.Saivyshnavi	K.Saivaishnavi	–	
16	5121	S. Nandini			Nandini	Nandini	Nandini	
17	5001				–	–	–	
18	5015	R. Umadevi			[illegible]	[illegible]	–	
19	5124							
20								
21								
22								
23								
24								
25								
26								
27								
28								
29								
30								
Total number of students attended							08	
Faculty Member Signature with Date								

Class In charge 20/1/24

HOD,ECE 20/1/24

Stanley College of Engineering & Technology for Women
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LIBRARY & INFORMATION CENTRE

Ref: SCETW/Lib/Cir/470/2024

Date:22.04.2024

CIRCULAR

All Heads of Departments are requested to submit the list of toppers (ie only one **Student** from every department) of **I, III & V Semesters** (irrespective of sections) by **25.04.2024**, for issuing **One Additional Library Card.**

LIBRARIAN

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STANLEY COLLEGE OF ENGINEERING ANDTECHNOLOGY FOR WOMEN (AUTONOMOUS), Hyderabad – 500 001

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

B.E. VI Semester

AY: 2022-2023

CIRCULAR

Ref. No.: SCETW/ECE-ACDC/2023/Cir-10 Date: 24-08-2023

The time table for Remedial classes for VI sem is scheduled as follows:

	9 am – 11 am	11 am – 1pm	1:40pm – 3:40 pm
26-Aug-23 (Saturday)	MPMC (Mrs. K. Bramaramba)	PLIC (Mrs. Y. Latha)	AWP (D. K. Prahalada Rao)
28-Aug-23 (Monday)	ED (Mrs. T. Prasanna)	PTSP (Mrs. V. Swetha)	S&S (Dr. K. N. Sahu)
29-Aug-23 (Tuesday)	ACS (Mrs. V. Swetha)	AC (Dr. G. Karthik)	AEC (Mrs. T. Prasanna)

For 24/8

Mrs. T. Prasanna
ACDC, Convener

24/08/23
HOD

Stanley College of Engineering & Technology for Women

Chapel Road, Abids, Hyderabad

(AUTONOMOUS)

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LIBRARY & INFORMATION CENTRE

Date:01.05.2024

All HODs, Faculty Members and Students are hereby informed that, the following students are eligible for one more additional library card in recognition of their outstanding academic performance, aimed at supporting their ongoing academic achievements.

LIST OF TOPPERS OF UG (I & V Sem) FOR 2023-24

S.No.	Branch	Section	Name of the Student	R.No.	SGPA	Sem
1	CSE	A	Anajpuram Shivani	160623733503	9.93	I
2	CSE	B	Sobiya Fatima	160623733120	9.93	I
3	CSE	C	Challa Sri Vidya	160623733137	9.7	I
4	AI & DS	A	Habeeba Khatoon	160623747018	9.73	I
5	-do-	A	Vanga Dikshitha Reddy	160623747046	9.73	I
6	AI & DS	B	Ansari Minuddin	160623747052	9.52	I
7	-do-	B	Adharapuraju Shree Vidhya	160623747050	9.52	I
8	CME	-	Shaik Thabassum	160623740049	9.7	I
9	ECE	A	Nandikanti Akshara	160623735041	9.83	I
10	ECE	B	Yeram Tulja	160623735112	9.7	I
11	IT	A	Sameena Sultana	160623737041	9.83	I
12	IT	B	Mahroon Begum	160623737060	9.35	I
13	IT	B	Vunna Jhansi Lakshmi	160623737125	9.35	I
14	CSE (AI & ML)	-	Pitani Mounika	160623748040	9.7	I
15	CSE	A	Sruthi	160621733063	9.12	V
16	CSE	B	Syeda Sadia Ahmed	160621733124	9.37	V
17	CSE	C	Kotolu Esha	160621733152	9.12	V
18	AI & DS	-	V Adri	160621747063	9.24	V
19	CME	-	Ayesha Naaz	160621740008	9.37	V
20	ECE	A	Chikka Tejaswini	160621735501	9.18	V
21	ECE	B	Bathini Jaya	160621735068	8.91	V
22	IT	A	Ibrahim Fatima	160621737012	9.39	V
23	IT	B	Sundhagani Vasanthi Goud	160621737306	8.78	V
24	EEE	-	K Mateswari	160621734302	8.98	V

LIST OF TOPPERS OF PG (I & III Sem) FOR 2023-24

S.No.	Branch	Name of the Student	R.No.	SGPA	Sem
1	M Tech (CSE)	S Architha	160623742115	9.28	I
2	ME (ES)	S Sai Jahnavi	160623744411	8.72	I
3	M Tech (CSE)	Vuligaddala Sathvika	160622742112	9.81	III
4	ME (ES)	Shaik Asifa	160622744404	9.33	III
5	MBA	Muna Sayeed	160622672032	9.63	III
6	-do-	Mudheerah Ferdous Sarhan	160622672063	9.63	III

LIBRARIAN

Copy to Principal/IQAC/All HODs - to circulate all faculty & students

Stanley College of Engineering & Technology for Women(A)

Chapel Road, Abids, Hyderabad.

Department of Electronics and Communications Enginering

List of Slow Learners

Section 1

Sl.No	Roll No
1	160622735001
2	160622735002
3	160622735006
4	160622735017
5	160622735019
6	160622735031
7	160622735033
8	160622735044
9	160622735048
10	160622735055

Section 2

Sl.No	Roll No
1	160622735081
2	160622735103
3	160622735109
4	160622735115
5	160622735117
6	160622735121
7	160622735124
8	160622735301
9	160622735307

List of Fast Learners

Section 1

Sl.No	Roll No
1	160622735003
2	160622735016
3	160622735021
4	160622735028
5	160622735030
6	160622735037
7	160622735040
8	160622735041
9	160622735047
10	160622735052
11	160622735053
12	160622735061
13	160622735070
14	160622735071

Section 2

Sl.No	Roll No
1	160622735070
2	160622735071
3	160622735076
4	160622735077
5	160622735087
6	160622735091
7	160622735092
8	160622735094
9	160622735100
10	160622735101
11	160622735102
12	160622735110
13	160622735112
14	160622735113
15	160622735114
16	160622735116
17	160622735305
18	160622735306
19	160622735313

Class Incharges

Mrs. T.Prasanna(SEC-1)

Mrs.G.Nirmala(SEC-2) 17/1/2024

HOD 17/1/24

STANLEY COLLEGE OF ENGINEERING AND TECHNOLOGY FOR WOMEN
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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Remedial Class Attendance for slow learners (III SEM ECE) A.Y: 2023-2024

Name of the Course: Electronic Devices & circuits Course Code: SPC301EC

Name of the Faculty Member: T. Prasanna

Topic Covered: h-Parameter model, MOSFET

S.No.	Roll Number	Student Name	Month	Jan	Jan			
			Date	17/1/24	24/1/24			
			Time					
				1	2	3	4	5
1	160622735002	A. vaishnavi		vaishnavi	vaishnavi			
2	160622735006	B. Geetha.			B. Geetha.			
3	160622735017	D. Shreya						
4	160622735019	Anusha			Anusha			
5	160622735031	Vaishnavi			Vaishnavi			
6	160622735033	[illegible]			[illegible]			
7	160622735044	M. Likhitha.			[illegible]			
8	160622735048	[redacted]						
9	160622735055	P. Kavya.			Kavya			
10	160622735081	C. Akshitha			Akshitha			
11	160622735103							
12	160622735109	N. Suraksh itha			[illegible]			
13	160622735117							
14	160622735301	A. Navya			Navya			
15	160622735307	K. Saivyshravi			[illegible]			
16	160622735001	Aamena Rafeeq			Aamena.			
17	115	UMA Rani			[illegible]			
18	121							
19	124	S. Deepika			Deepika			
20								
21								
22								
23								
24								
25								
26								
27								
28								
29								
30								
	Total number of students attended							
	Faculty Member Signature with Date				24/1/24			

Class In charge

24/1

HOD,ECE

STANLEY COLLEGE OF ENGINEERING AND TECHNOLOGY FOR WOMEN
DEPARTMENT OF INFORMATION TECHNOLOGY
B.E IT V SEMESTER SECTION A LIST OF FAST LEARNERS
SUBJECT : COMPILER CONSTRUCTION

ROLL NO	NAME	Marks out of 40	Actions Taken
160621737002	Adiba Maheen	40	Seminars , Advanced Lab Exercise Problems and Case studies were given to students
160621737004	Akkala Soumya	40	
160621737005	Aleena Fatima	40	
160621737009	Cherupalli Srinidhi	37	
160621737011	Dasari Sai Prasanna	36	
160621737012	Deebah Fatima	39	
160621737013	Diddy Vaishnavi	36	
160621737015	Emmadi Akshaya	38	
160621737016	G D Sreevalli	37	
160621737017	G Manisha	37	
160621737019	Janya Maila	36	
160621737020	Kodumuru Lahari	36	
160621737022	Kura Sai Sreeja	40	
160621737024	Lubna Afsar	36	
160621737028	Mehrajunnisa Begum	39	
160621737029	Moosaram Thota Trisha	37	
160621737034	Nanavath Vaishnavi	36	
160621737038	Parsawar Rakshitha	40	
160621737039	Pobbathi Amaravathi	39	
160621737043	Safooraa Amjad Khan	39	
160621737044	Sania Waseem	40	
160621737048	Sukrithi Mathur	36	
160621737050	Syeda Maimona Jaffer	38	
160621737051	Syeda Omema	35	
160621737052	Syeda Tooba Samreen	37	
160621737057	Umama Dinaz	37	
160621737058	Uppala Shreshta	36	

I/C Faculty : Mrs. T C Swetha Priya
Assistant Professor
IT Department

HOD

Name: K.SAI SREEJA

Roll Number: 160621737022

Case Study: Real-Time Financial Trading System with C

1. Introduction

In the fast-paced world of financial markets, the development of a real-time financial trading system is critical for executing trades swiftly and efficiently. This case study explores the creation of a real-time financial trading system using the C programming language. The objective is to showcase how C's efficiency, low-level control, and high-performance capabilities contribute to the development of a reliable and responsive trading platform.

2. Project Overview

The project involves designing and implementing a financial trading system capable of processing market data, executing trades, and managing risk in real-time. The system aims to provide traders with a responsive and efficient platform for making split-second decisions in the volatile financial markets.

3. System Architecture

- Market Data Feed: The system integrates with real-time market data feeds, receiving and processing information on stock prices, market trends, and other relevant data.
- Algorithmic Trading Engine: C is used to develop the core trading algorithms that analyse market data and execute trades based on predefined strategies.
- Risk Management: The system includes robust risk management modules, implemented in C, to monitor and control potential financial risks associated with trading activities.

4. Challenges and Solutions

- Low-Latency Requirements: C's low-level capabilities and efficient memory management are crucial for meeting the low-latency requirements of financial trading, ensuring rapid execution of trades.
- Concurrency and Multithreading: The system employs C's support for concurrency and multithreading to handle multiple market data streams and execute trades concurrently.

5. Case Study Focus: High-Frequency Trading Platform

Imagine the trading system is implemented as a high-frequency trading (HFT) platform, where the speed of execution is critical for taking advantage of fleeting market opportunities.

- Microsecond-level Latency: The system, developed in C, achieves microsecond-level latency to ensure rapid response to market changes.
- Automated Trading Strategies: C-based algorithms implement automated trading strategies, allowing the system to execute trades automatically based on market conditions.

6. Results and Benefits

- Real-Time Responsiveness: The C-based trading system provides real-time responsiveness, allowing traders to capitalize on market opportunities swiftly.
- Scalability: C's efficiency enables the system to scale with increasing trade volumes and data complexity.
- Reliability: The robustness of C contributes to the overall reliability of the trading platform, minimising the risk of system failures.

7. Conclusion

In conclusion, the case study demonstrates the application of the C programming language in developing a high-performance, real-time financial trading system. C's efficiency, low-level control, and suitability for high-frequency applications make it a preferred choice for building reliable and responsive platforms in the competitive landscape of financial markets.

Case Study: Development of a Smart Home Automation System with C++

1. Introduction

The smart home automation industry is rapidly evolving, offering homeowners enhanced control and efficiency in managing their living spaces. This case study explores the development of a Smart Home Automation System using the C++ programming language. The goal is to showcase how C++'s efficiency, object-oriented design, and low-level control contribute to the creation of an intelligent and user-friendly home automation solution.

2. Project Overview

The project involves designing and implementing a Smart Home Automation System that integrates with various devices, sensors, and actuators within a home environment. The system aims to provide users with a centralised platform for managing and monitoring aspects such as lighting, climate control, security, and entertainment.

3. System Architecture

- Object-Oriented Design: C++ is utilised to design an object-oriented architecture, facilitating the development of modular and reusable components for different smart home devices.
- Device Integration: C++ enables seamless integration with diverse smart home devices, including IoT devices, sensors, smart thermostats, smart locks, and more.
- Low-Level Control: C++'s low-level control is employed for precise management of hardware components, ensuring real-time responsiveness and optimal resource utilisation.

4. Challenges and Solutions

- Device Heterogeneity: C++ allows for the creation of abstract device interfaces, accommodating a wide range of devices with varying functionalities.

- Real-Time Responsiveness: C++'s low-level capabilities are crucial for achieving real-time responsiveness, ensuring quick and reliable interactions between user commands and device actions.

5. Case Study Focus: Intelligent Climate Control and Security System

Imagine the Smart Home Automation System is designed to intelligently control the climate and enhance security within the home.

- Climate Control: C++-based algorithms analyse sensor data and user preferences to dynamically adjust heating, ventilation, and air conditioning (HVAC) settings for energy efficiency and comfort.
- Security System: C++ is utilised to implement features such as smart door locks, motion sensors, and surveillance cameras, providing real-time alerts and remote monitoring capabilities.

6. Results and Benefits

- Efficient Device Integration: C++'s object-oriented design allows for efficient integration with various smart home devices, providing users with a unified and user-friendly interface.
- Real-Time Automation: The Smart Home Automation System, developed with C++, offers real-time responsiveness and automation, enhancing user convenience and energy efficiency.
- Scalability: C++'s efficiency contributes to the system's scalability, allowing homeowners to expand their smart home ecosystem by seamlessly adding new devices.

7. Conclusion

In conclusion, the case study illustrates the application of the C++ programming language in developing a Smart Home Automation System. C++'s efficiency, object-oriented design, and low-level control play a crucial role in creating an intelligent, responsive, and scalable solution that meets the diverse needs of homeowners in the realm of home automation.

STANLEY COLLEGE OF ENGINEERING AND TECHNOLOGY FOR WOMEN
DEPARTMENT OF INFORMATION TECHNOLOGY
B.E IT V SEMESTER SECTION A LIST OF SLOW LEARNERS
SUBJECT : COMPILER CONSTRUCTION

ROLL NO	NAME	Marks out of 30	Actions Taken
160621737001	Adepu Punitheeshwari	11	Previous Question Papers were given as exercises. Tests were conducted
160621737018	Gundu Vasavi	14.5	
160621737021	Koyyada Niveditha Goud	18.5	
160621737025	M Sai Thrisha	18	
160621737032	N Saloni Meera	17	
160621737035	Paladugu Gayathri	19	
160621737036	Palla Sashi Rekha	10	
160621737042	Safa Naaz	17	
160621737045	Sheela Anand Asade	12	
160621737047	Siripurapu Akshitha	15.5	
160621737062	Yellamala Divya	11.5	
160621737063	Zainab Farooq	18	
160621737064	Mallu Yuktha Reddy	10	
160621737065	Ravulapelly Savarnika	12.5	
160621737305	M. Sreeya	13	

I/C Faculty
Mrs. T C Swetha Priya
Assistant Professor
IT Department

HOD

SAQ CC A.Punitheeshwari

160621737001.

8/15

6/12/23

1. SDD & SDT
2. What is Symbol table? What are it's contents?
3. What is an activation record? What are it's constraints? Draw its formats?

~~LAQ~~
LAQ

1) Type checking & type conversion
2) Explain in detail about Data structures Used in Symbol table
3) Explain in detail about semantic processing Transformation

Answers:-

1A) SDD Syntax Directed Defination is a kind of abstract specification. it is generalization of content free grammar in which each grammar production $x \to a$ is associated with it a set of production rules of the form $s = f(b_1, b_2, \ldots b_k)$ where s is the attribute obtained from function.
SDT Syntax Directed Translation parser uses a CFG to validate the input string and produce o/p for the next phase of the compiler. o/p can be either a parse tree or an abstract syntax tree.

2A) The Symbol table is defined as the set of Name and Value pairs.
Contents of Symbol table:-
Variable names and constants
Procedure and function names
Literal constants and strings
Compiler generated temporaries
Labels in Source Languages.

3A) The activation record is a ~~cont~~ block of memory that manages the execution of a procedure or function. It is also known as a stack frame or function call frame.

LAQ

1A) Type checking:- It is a process of verifying & enforcing constraints of types in values. A compiler must check that the source program should follow the stack compiler checking the following compiler prgm must check that the source program should follow the syntactic & semantic conversions of the source language and it should also check the type rules of the languages. The data types like INTEGER, FLOAT, CHARACTER and all the other ~~languages~~ data types is maintained and computed by the compiler.

· Conversion:-
conversion from one type to another type is known as implict if it is to be done automatically by the compiler. Implict type conversions are also called coercion and coercion is limited in many languages.
ex:- An integer may be converted to a real but real is not converted to an integer.

2A) The Symbol table is defined as the set of Name and value pairs. It is an important data structures created & maintained by the compiler in order to keep track of semantics of variables i.e it stores information about the scope and binding information about names, information about instances of various entities such as variable and function names, classes, objects etc.

160621737036
IT-A
Sashirekha.

10/15

SAQ's

1) SDD and SDT
2) What is symbol table? what are its contents?
3) What is an activation record? What are its content? Draw its format.

LAQ's

1) Type checking and Type Conversion.
2) Explain in detail about Data Structures used in Symbol table
3) Explain in detail about semantic p. ... Transformations.

Answers

1) SDD

SDD is known as syntax directed defination.. A SDD specifies the values of attribution by associating the syntax semantic rules with the grammar productions. An infinite infix to prefix expression or translation can have a production rule as follows

Production	Semantic rule
$E \rightarrow G + T$	$E \cdot code = G \cdot code \,\|\|\, T \cdot code \,\|\|\, '+'$

The production has 2 non-terminals G, T with the sub-script in E_1 distinguish the occurance of E in the production body form the head both T & E having string valued attribute

SDT is known as syntax directed translation embeded program fragments called Semantic actions. within production on boding as shown

$E \rightarrow E_1 + T$ { print '+' }

These semantic actions are embeded in the curly braces. and if any gramm symbol occour we enclose them within single counter.

2. <u>Symbol Table</u>:- It is a data structure used by the compiler to keep track of semantics of Variable. Symbol table stores the information about scope & binding information about the maner. A symbol table consits of the following

1) Variable names 2) Constants 3) Procedure names 4) Function names

5) Compiler gnerated tempories 6) Litural constants & strings 7) tables in source lang

LAQ's

2. Data Structures used in symbol table are as follows

1) Linear list
2) Trees
3) Hash tables

In linear lists the nodes are insulted. & transformed from begining

The operations that can be performed on a linear lists are

→ Search :- Search linearly from begining to end. If the element is found then stop.

Adding / Insertion :- First search whether the node exists. If the node is found. then stop

2) <u>Trees</u>.

A symbol table can be in the form of trees

A tree based symbol Table as follows.

```
class {
  int x;
void func1(...) {...}
void func2 (...) {
void func3 (...) {
float x;
}
int func4(...) {
 double x, y;
 ....
 func1(x);
 }
}
}
```

3) Hash Table.

The operation performed on hash table are

1) Search :- Hash the name in the hash function

Hash (symbol) $\in$ 0, k-1 where k = table size.

2) Insertion: perform ~~showing~~ searching & then inserting at

the end of the symbol table.

Global Symbol Table.

Name	attr	....	link.
A.			

symbol table for A.

name	attr	....	link.
x	int		
fun1	fnt		
fun2	fnt		

symbol table perform-2

Symbol table perfom 1

1606121137018
G. Vasavi

① Describe the role of lexical analyzer in complier design?

② Define Ambigous Grammar

LAQ

15/15 5/10/20

③ Construct recursive decent parser for the following grammar.

$E \rightarrow E+T / T$

$T \rightarrow T*F / F$

$F \rightarrow (E) / id$

④ Construt predicitive ~~grammar~~ Parse for the grammar

$S \rightarrow (L) / a$

$L \rightarrow L,S / S$

Answer

1) The phase of a compiler is the lexical analyzer:-

* The main task of the lexi analyzer is to read the input character of the source program. group them to lexems and produces as out put as a sequence of a token of lexical analyzer

*. The stream of the token is the Syntax analysis

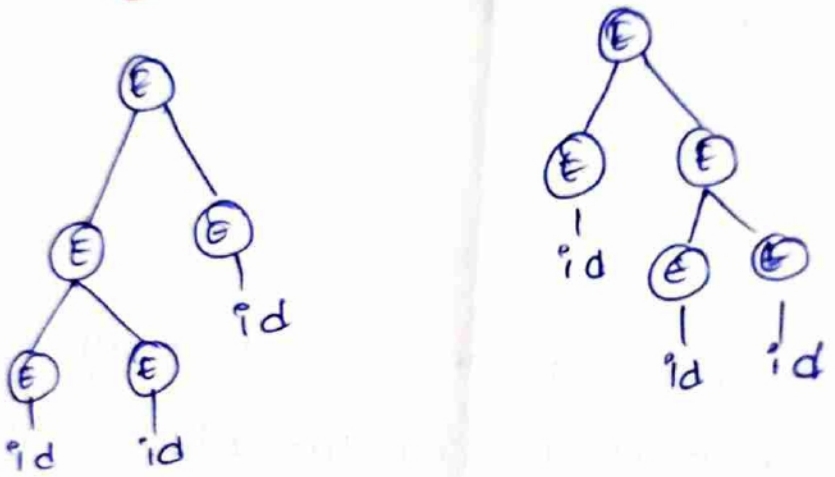
*. It is common for lexical analyzer to interact the ~~lexical analyzer~~ withe the table as well.

* whenever the lexical analyzer discover the lexeme constructing the identifier need to entry into the lexeme into the symbol table.

*. The information regarding the kind of identifier may be read for the Symbol table by lexical analyzer to determine proper token.

(2) Ambiour grammar :- G(V, T, P, S) is a CFG that is to be ambiguous if and only if there exists a st in T* that has more than one parse tree, where V is finite set of variable. T is a ~~finite tree~~ more than one parse tree where V is an finite set of set of variables. T is finite set of form A → a where A is variable and a ∈ (V∪T)* S is designed variable called the start symbol

Ex:



(3)

E → E+T | T
T → T*F | F
F → (E) | Id

~~E → E+T | T~~
~~T → T E'~~
~~E → E*T | T~~
~~T → T E'~~
~~E' → +T E' | ε~~

~~T → F T'~~
~~T' → *T' | ε~~
~~E → (E) | Id~~

```
(5) procedure E
{
T()
if (lookahead = '(') then
{
match (+)
E();
}
Else
Error;
if (lookahead = '$')
{
declare success;
}
procedure (T)
{
if (look ahead = *) Then
{
match (*);
T();
}
Else.
{
Error;
}
procedure (F)
if (look ahead = id)
{
match (id)
}
Else.
Error;
procedure Error()
{
print ("Error")
}
```

4)

$S \to (L) \mid a$

$L \to L, S \mid S$

$(S) \to (L) \mid a$

$L \to SL'$

$L' \to SL' \mid$

FIRST(S) = {(, a}

FIRST(L) = FIRST(S) = {(, a}

FIRST(L') = {, , ε}

FOLLOW(S) = ε · FIRST(L') = {ε} ∪ {FIRST(L')} =

FOLLOW(L) = FOLLOW(S) = {)}

FOLLOW(L') = FOLLOW(L) = {)}

	(	)	a	,
S	S → (L)		S → a.	
L	L → SL'		L → SL'	
L'		L' → ε		L' → SL'

Stanley College of Engineering & Technology for Women

Chapel Road, Abids.

Department of Humanities and Sciences

Date: 13.11.2023

CIRCULAR

All the faculties are informed that they have to identify the slow learners and conduct remedial classes for them, for improving their performance. The remedial hour is to be conducted before the second internal exams.

Timings 4.30pm to 5.30 pm

Monday	Mathematics -II
Tuesday	Engineering Physics
Wednesday	Engineering Chemistry
Thursday	BEEC
Friday	PPS & English

SUBJECT	FACULTY
Mathematics -II	Dr. K.L Vasaundhara/ V.Mythreye/ G Shirisha/ M Vidhya Bhargavi/ A.Anusha / K.Vaishnavi
Physics	G. Padmasree/ J.P Pramod/ P Anusha
Chemistry	Dr. K Nagi Reddy/ M Sharada devi/ R Gangadhara/ B. Srilatha
BEEC	S Suman/ Vijaya Lakshmi
PPS	L Tirupathi Reddy/ C Kishore Kumar Reddy/ Sumayya Afreen
English	Miss. Virgilia Richard/ Dr. V Srilatha/ Haripriya H/ Athul Mohan T M/ Heena Khan/ Gaiety

HOD (H&S)

Stanley college of Engineeirng & Technology for women
Chapel Road, Abids

REMEDIAL CLASSESS FOR SLOW LEARNERS (2023-24 SEM II)

Subject: Engineering Mathematics-II AY: 2023-24

Code: SBS0201MT SEM : II

CSE A

S. NO	HALL TICKET NUMBER	MID I MARKS	Action taken	MID II MARKS
1	160623733001	6	Remedial classes conducted	13
2	160623733002	5		12
3	160623733010	5		5
4	160623733011	1		5
5	160623733015	3		10
6	160623733016	2		10
7	160623733025	2		2
8	160623733026	3		2
9	160623733029	5		25
10	160623733035	4		4
11	160623733037	9		25
12	160623733038	6		15
13	160623733041	4		11
14	160623733043	1		4
15	160623733046	4		8
16	160623733048	3		9
17	160623733051	3		6
18	160623733052	6		14

CSE B

S. NO	HALL TICKET NUMBER	MID I MARKS	Action taken	MID II MARKS
1	160623733066	4	Remedial classes conducted	23
2	160623733067	8		14
3	160623733071	3		16
4	160623733072	3		19
5	160623733076	4		11
6	160623733082	9		22
7	160623733084	5		25
8	160623733086	2		24
9	160623733087	4		3
10	160623733088	3		3
14	160623733092	4		18
15	160623733094	2		25
16	160623733096	4		25
17	160623733098	5		25
18	160623733100	3		24
19	160623733101	9		22
20	160623733102	1		15
21	160623733103	7		25
22	160623733106	7		15
23	160623733108	3		14
24	160623733115	6		25
25	160623733116	2		5
26	160623733117	2		21
27	160623733119	7		23
28	160623733121	6		21
29	160623733123	9		24
30	160623733125	3		4
31	160623733126	7		21
32	160623733127	2		7

CSE C

S. NO	HALL TICKET NUMBER	MID I MARKS	Action taken	MID II MARKS
1	160623733129	6	Remedial classes conducted	10
2	160623733131	8		15
4	160623733135	2		1
5	160623733138	2		5
6	160623733139	4		17
7	160623733141	8		3
8	160623733143	7		13
9	160623733146	4		10
10	160623733152	4		13
11	160623733154	5		2
12	160623733159	7		16
13	160623733161	5		20
14	160623733164	4		3
15	160623733167	7		2
16	160623733168	7		11
17	160623733169	4		4
18	160623733170	2		12
19	160623733173	8		22
20	160623733177	7		9
21	160623733181	5		12
22	160623733182	6		8
23	160623733183	3		3
24	160623733184	7		12
25	160623733187	8		13
26	160623733189	3		2
27	160623733191	3		4

CME

S. NO	HALL TICKET NUMBER	MID I MARKS	Action taken	MID II MARKS
1	160623740002	2	Remedial classes conducted	8
2	160623740005	2		7
3	160623740008	7		7
4	160623740009	9		22
5	160623740012	4		13
6	160623740013	2		7
7	160623740016	6		21
8	160623740017	8		24
9	160623740019	3		22
10	160623740020	9		15
11	160623740021	8		20
12	160623740023	4		12
13	160623740024	2		25
14	160623740025	6		22
15	160623740027	5		11
16	160623740029	9		19
17	160623740030	5		22
18	160623740031	6		22
19	160623740032	5		15
20	160623740034	4		18
21	160623740035	4		10
22	160623740036	7		7
23	160623740038	5		23
24	160623740039	6		24
25	160623740040	4		9
26	160623740041	4		23
27	160623740043	2		8
28	160623740044	3		9
29	160623740050	5		5
30	160623740051	5		20
31	160623740052	9		14
32	160623740054	6		7
33	160623740055	6		8
34	160623740059	5		23
35	160623740062	2		11
37	160623740064	1		0

AIML

S. NO	HALL TICKET NUMBER	MID I MARKS	Action taken	MID II MARKS
1	160623748004	4	Remedial classes conducted	8
2	160623748005	8		20
3	160623748006	5		19
4	160623748013	3		16
5	160623748015	4		23
6	160623748016	3		24
7	160623748017	5		13
8	160623748018	0		4
9	160623748019	1		10
10	160623748020	6		22
11	160623748021	3		9
12	160623748022	6		24
13	160623748024	2		0
14	160623748025	2		17
15	160623748027	4		14
16	160623748028	5		13
17	160623748029	5		17
18	160623748031	7		21
19	160623748032	1		8
20	160623748033	4		24
21	160623748034	3		16
22	160623748035	2		20
23	160623748036	3		8
24	160623748037	3		1
25	160623748038	3		9
26	160623748041	7		17
27	160623748043	2		10
28	160623748045	1		24
29	160623748046	7		22
30	160623748047	3		19
31	160623748049	4		22
32	160623748051	6		25
33	160623748052	0		0
34	160623748053	5		21
35	160623748054	4		10
36	160623748055	7		25
37	160623748056	3		20
38	160623748057	6		6
39	160623748063	7		4
40	160623748064	6		24

EEE

S. NO	HALL TICKET NUMBER	MID I MARKS	Action taken	MID II MARKS
1	160623734002	9	Remedial classes conducted	19
2	160623734003	9		14
3	160623734004	8		11
4	160623734005	1		7
5	160623734006	8		10
6	160623734007	7		6

AIDS-A

S. NO	HALL TICKET NUMBER	MID I MARKS	Action taken	MID II MARKS
1	160623747001	9	Remedial classes conducted	15
2	160623747009	8		17
3	160623747010	7		23
4	160623747011	4		6
5	160623747022	7		25
6	160623747023	8		16
7	160623747025	5		2
8	160623747031	8		4
9	160623747034	8		22
10	160623747036	7		17
11	160623747045	6		9
12	160623747048	5		8

AIDS-B

S. NO	HALL TICKET NUMBER	MID I MARKS	Action taken	MID II MARKS
1	160623747049	2	Remedial classes conducted	8
2	160623747051	8		25
3	160623747056	6		13
4	160623747057	5		15
5	160623747059	5		15
6	160623747061	6		7
7	160623747063	7		20
8	160623747064	2		3
9	160623747066	1		19
10	160623747067	3		3
11	160623747068	6		25
12	160623747069	7		23
13	160623747071	4		19
14	160623747072	2		9
15	160623747073	0		2
17	160623747078	1		3
18	160623747080	8		4
19	160623747082	4		8
20	160623747083	1		12
21	160623747084	9		7
22	160623747086	7		17
23	160623747087	0		3
24	160623747090	5		22
25	160623747091	3		20
26	160623747092	8		25

IT-A

S. NO	HALL TICKET NUMBER	MID I MARKS	Action taken	MID II MARKS
1	160623737001	6	Remedial classes conducted	8
2	160623737005	6		10
3	160623737007	4		8
4	160623737014	8		22
5	160623737015	5		3
6	160623737018	9		17
7	160623737019	6		18
8	160623737023	4		19
9	160623737026	1		6
10	160623737027	4		9
11	160623737028	3		5
12	160623737029	1		10
13	160623737031	1		11
14	160623737032	8		24
15	160623737034	2		6
16	160623737035	5		12
17	160623737036	8		15
18	160623737037	1		1
19	160623737044	9		17
21	160623737049	3		21
22	160623737051	3		18
23	160623737052	8		21
24	160623737053	3		16
25	160623737054	2		9
26	160623737055	2		2
27	160623737056	7		24
28	160623737061	1		12
29	160623737065	4		4

IT-B

S. NO	HALL TICKET NUMBER	MID I MARKS	Action taken	MID II MARKS
1	160623737067	3	Remedial classes conducted	17
2	160623737068	5		23
3	160623737070	3		17
4	160623737072	4		25
5	160623737073	3		23
6	160623737074	1		12
7	160623737075	7		22
8	160623737076	4		2
9	160623737077	5		7
10	160623737079	3		13
11	160623737080	6		17
12	160623737081	5		10
13	160623737082	2		24
14	160623737083	2		22
15	160623737085	5		15
16	160623737086	9		25
17	160623737087	3		19
18	160623737088	3		14
19	160623737091	5		14
20	160623737092	6		25
21	160623737093	0		25
22	160623737094	0		22
23	160623737095	7		25
24	160623737096	4		16
25	160623737098	0		21
26	160623737099	3		22
27	160623737100	3		25
28	160623737101	1		25
29	160623737102	8		17
30	160623737104	6		25
31	160623737105	8		24
32	160623737106	0		2
33	160623737110	2		19
34	160623737112	4		24
35	160623737113	7		21
36	160623737114	5		22
37	160623737115	8		20
38	160623737119	7		15
39	160623737120	6		21
40	160623737121	2		22
41	160623737122	2		24
43	160623737127	8		23

ECE-A

S. NO	HALL TICKET NUMBER	MID I MARKS	Action taken	MID II MARKS
1	160623735001	4	Remedial classes conducted	3
2	160623735002	5		15
3	160623735003	8		23
4	160623735005	6		21
5	160623735006	5		6
6	160623735007	3		4
7	160623735008	3		20
8	160623735009	6		15
9	160623735010	2		6
10	160623735011	3		7
11	160623735012	3		25
12	160623735013	3		12
13	160623735016	2		16
14	160623735017	4		22
15	160623735020	8		25
16	160623735021	8		22
17	160623735022	2		20
18	160623735024	2		9
19	160623735025	2		13
20	160623735027	2		13
21	160623735030	7		18
22	160623735031	9		15
23	160623735032	3		13
24	160623735034	5		6
25	160623735036	5		10
26	160623735037	4		16
27	160623735040	6		25
28	160623735043	2		6
29	160623735046	7		11
30	160623735048	2		10
31	160623735050	8		19
32	160623735051	9		5
33	160623735052	2		24
34	160623735053	3		22
35	160623735054	6		22
36	160623735058	2		23
37	160623735059	5		11
38	160623735061	6		5
39	160623735063	8		11
40	160623735064	5		13

ECE-B

S. NO	HALL TICKET NUMBER	MID I MARKS	Action taken	MID II MARKS
1	160623735070	8	Remedial classes conducted	7
2	160623735077	7		9
3	160623735078	6		18
4	160623735079	8		25
5	160623735080	8		24
6	160623735081	0		12
7	160623735086	4		5
8	160623735089	7		21
9	160623735090	3		24
10	160623735094	7		5
11	160623735096	2		7
12	160623735098	9		23
13	160623735099	0		11
14	160623735100	2		9
16	160623735106	8		13
17	160623735110	7		8
18	160623735111	6		20
19	160623735114	8		3
20	160623735115	9		15

Stanley College of Engineering & Technology for Women

Department of MBA

MBA III Sem list of slow learners 2023-24

S.No	Roll Number	Name of Student	Name of the Subject
1	160622672003	Ailla Joshna Goud	OM
2	160622672016	Hajera Ansari	
1	160622672042	Polapalli Sirisha	EB
1	160622672042	Polapalli Sirisha	QTBD
2	160622672043	Ponnaluri Anu Priya	
3	160622672014	G Sukhjeevan Rani	
1	160622672042	Polapalli Sirisha	IM
2	160622672044	Radhika G	
1	160622672014	G Sukhjeevan Rani	CM
2	160622672038	Pagilla Pravalika	
1	160622672022	K Pooja Kailash	IF
2	160622672042	Polapalli Sirisha	

MBA I Sem list of slow learners 2023-24

S.No	Roll Number	Name of Student	Name of the Subject
1	160623672001	ACHHA SRAVANI	MOB
2	160623672006	AYESHA JABEEN	
3	160623672054	AMANDEEPKAUR	
4	160623672058	FAIZA FATIMA	
5	160623672073	M. VINEETHA	
1	160623672001	ACHHA SRAVANI	AFM
2	160623672046	SYEDA MADEEHA TABASSUM	
3	160623672053	AKULA DEEKSHITHA	
4	160623672054	AMANDEEPKAUR	
5	160623672080	V SOWMYA	
1	160623672018	GUMMADIVELLI KRISHNAVENI	MM
1	160623672004	AMENA KAUSAR	BRM
2	160623672006	AYESHA JABEEN	
3	160623672007	AYESHA ROOHI	
4	160623672021	JUWAIRYA FATIMA	
5	160623672045	SEREPALLI SOWMYA	
6	160623672054	AMANDEEPKAUR	
1	160623672014	ESHWARI KANAGALA	ME
1	160623672001	ACHHA SRAVANI	BC
2	160623672035	P ANITHA	
3	160623672053	AKULA DEEKSHITHA	
4	160623672073	M. VINEETHA	

HoD

Uni IV Multinational Corporate Decisions in Global Markets

1. What is home country bias?
2. What are the modes of FDI?
3. What are the advantages of FDI?
4. What is eclectic paradigm?
5. Explain Comparative Advantage
6. What is the scope of International Financial Management?
7. Differentiate domestic financial management and international financial management
8. What are motives of FDI?
9. What are the barriers to International Portfolio Diversification?
10. What is International Portfolio Diversification?
11. Describe the key components of an MNC's capital
12. Identify the factors that affect an MNC's capital structure
13. Explain the interaction between a subsidiary and parent in capital structure decisions
14. Explain how the cost of capital is estimated
15. Explain why the cost of capital varies among countries
16. What makes the APV capital budgeting framework useful for analyzing foreign capital expenditures?

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Slow and Fast Learners action taken

- Previous Question papers were discussed
- Important Questions unit wise are given for all the units
- For Fast learners students are encouraged to write papers in various reputed journals.
- Additional classroom activities such as Group discussions and case studies and various Group activities to improve their team building capabilities.

HoD

Unit V International Tax Environment

1. Explain the term netting
2. Explain CFC
3. What are the Organizational Structures for Reducing Tax Liabilities?
4. What are the types of taxation?
5. What is income tax?
6. Explain withholding tax
7. What is value added tax ?
8. Explain Tobin tax
9. Explain worldwide approach
10. Explain territorial approach
11. Explain Foreign tax Credits
12. What are tax havens?
13. What are offshore financial centres?
14. Explain reinvoicing centre
15. What is a Tax Haven? Give Examples
16. What are the objectives of taxation?
17. Explain tax neutrality
18. What is tax equity?

in its price to its most reasonable value. Under EMH, the stock are always traded at their sufficient fair value on stock exchanges, and it is impossible for the general investors to either purchase depreciated value stocks or sell stocks for inflated prices because in efficient market at any point of time the actual price of a security will be a good estimate of its intrinsic value. It is impossible to outperform the stock market, through expert stock selection or market timing. The only way for an investor to earn higher returns is by investing is riskier securities.

2. Variants of Efficient Market Hypothesis

There are three variants of the efficient market hypothesis: 1. Weak form Efficiency or Random Walk Theory, 2. Semi-Strong Efficiency and 3. Strong form Efficiency

(i)Weak form Efficiency

Weak form efficiency, also known as the random walk theory, states that future securities' prices are random and not influenced by past events and investors can not earn super-normal profits (excess profits) from trading strategies based on past prices or returns.

(ii)Semi-Strong Efficiency

The semi-strong form of Efficiency claims both that prices reflect all publicly available information and that prices instantly change to reflect new public information such as market information of the past returns, prices and trading volumes of the stock and non-market information such as earnings, dividend announcements, other ratios and news about the overall economy. Investors, who based their decisions on the information that becomes public, cannot gain above-average returns and only traders with access to non-public information can earn excess profits.

(iii)Strong Form Efficiency

The strong form of efficiency additionally claims that prices instantly reflect even secret information, which is not available to all market participants.

3. Need for the Study

The stocks listed on the SENSEX are tested for weak form efficient market hypothesis, give valuable insight on the randomness of stock prices prevailing in the Indian stock market, and the existence of any significant relation between the past security prices and the future security prices. With this relationship, investors can determine if it is worthwhile to analyse the past stocks before making investments in them. The extent of inefficiency of the Indian stock market also determines how useful professional stock picking is to the potential investors.

4. Scope of the Study

The present study aims to analyse the Indian Stock market for Weak form efficiency, based on the theory of Efficient Market Hypothesis given by Eugene Fama. For this study, the weekly closing prices of the securities of the companies

Efficient Market Hypothesis in the Indian Stock Market

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Abstract

The investment decision is made under the simple premise that the securities are worth more than their current price, while securities that they are selling are worth less than the selling price. However, if markets are efficient and current prices fully reflect all information, then trading securities is an attempt to outperform the market and will effectively be a game of chance rather than skill. The Efficient Market Hypothesis (EMH) states that at any given time, security prices fully reflect all available information based on various factors like company disclosures, company announcements, dividend policy of company etc., and these factors are also used as a tool to predict the future prices of stocks. The focus of the present study is on the Weak form Efficiency, which is one of the three different degrees of EMH. Weak form Efficiency claims that past price movements do not affect a stock's price and cannot be used to predict its future movement of the price. This study attempts to test the companies listed on the SENSEX of the Bombay Stock Exchange for Weak Form EMH for the period from 1st April 2017 to 31st March 2018. By implementing parametric and non-parametric statistical tools like the Autocorrelation, Correlogram, Runs Test, along with descriptive statistics on the weekly closing prices of the 31 companies listed on the SENSEX, an attempt is made to find out whether there is any relationship between the future prices of stock and their past performance through EMH.

Keywords: *Efficient Market Hypothesis, Equity, India, Sensex, Stock Market, Weak form*

1. Introduction

When a market is said to be efficient, it means that the prices of the securities reflect their actual worth. The market incorporates all information into the prices of the securities in a rapid and unbiased manner. In other words, a market in which the prices of securities reflect fully all available information is called an efficient market.

The efficient market hypothesis (EMH) is an investment theory proposed by Eugene Fama, in the 1970's. It states that the security prices fully reflect entire available information is based on various factors like company disclosures, company announcements, dividend policy of firm, firm fundamentals and changes in the government policy etc.

According to the EMH theory, the price of a financial instrument (bond, share, etc.) reflects all the information currently available. The main reason for this perfect price of securities is that, if one stock happens to trade even a little cheap or too costly, then its demand increases or decreases, leading to a rapid movement