

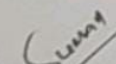
Stanley College of Engineering & Technology for Women

Chapel Road, Abids, Hyderabad

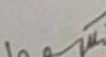
Date: 18.02.2021

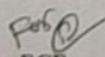
Examination circular

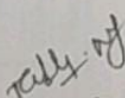
1. The II Internal Examinations (online) for B.E- III & V sem are being scheduled from **01st March 2021**.
2. The concerned faculty is requested to submit the question papers (hard and softcopies - 2 sets in which either of them will be selected) through HOD/dept. exams in-charges to the Exam branch on or before **24th Feb. 2021 before 02.30PM without fail**.
3. The faculty is expected to follow the guidelines of University in setting the question paper.
4. Faculty should mention the new Blooms Taxonomy and CO, PO mapping on the Question paper in the tabular form. No deviation is entertained.
5. The HODs are requested to verify the submission of all subject Question papers on time.
6. The HOD/exam in-charges are requested to submit the list of online invigilators to the exam branch as per the request on or before **24th Feb. 2021 without fail**.

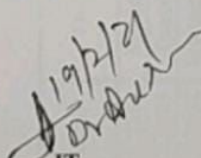

I/C Exams

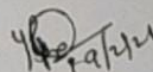
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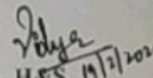

CSE

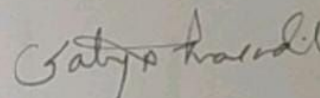

ECE


EEE


IT


MBA


H&S 19/2/2021


Principal

Stanley College of Engineering & Technology for Women

Abids, Hyderabad.

B.E. - III & V Sem - II - INTERNAL EXAMINATION TIME TABLE for the A.Y - 2020 - 2021

Date: 18.02.2021

Date	Day	CSE III Sem		ECE III Sem		EEE III Sem		IT III Sem		CME III Sem	
		09:30 AM - 10:30 AM	10:30 AM - 11:30 AM	09:30 AM - 10:30 AM	10:30 AM - 11:30 AM	09:30 AM - 10:30 AM	10:30 AM - 11:30 AM	09:30 AM - 10:30 AM	10:30 AM - 11:30 AM	09:30 AM - 10:30 AM	10:30 AM - 11:30 AM
1.3.2021	Monday	BE	DE	PTSP	ETCE	IP	BIO-E	BE	MFTT	BEE	LST
2.3.2021	Tuesday	DSA	DST	NT	DE	E Mechanics	ESE	DE	M-III	DS	DSML
3.3.2021	Wednesday	OR	PL	ED	F&A	ECA	EMF	DS	ETCE	OR	P&S
4.3.2021	Thursday		Bio E			AE		F & A			Bio E

Date	Day	CSE V Sem		ECE V Sem		EEE V Sem		IT V Sem	
		09:30 AM - 10:30 AM	10:30 AM - 11:30 AM	09:30 AM - 10:30 AM	10:30 AM - 11:30 AM	09:30 AM - 10:30 AM	10:30 AM - 11:30 AM	09:30 AM - 10:30 AM	10:30 AM - 11:30 AM
1.3.2021	Monday	SE	OS	MPMC	AWP	EM-II	PS-I	WAD	OS
2.3.2021	Tuesday	ALC	AI	DSP	AC	LCS	MPMC	T(A)	CN
3.3.2021	Wednesday	WT	CG	ACS		S&S	RES	SE	AI

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I/Charge Exams

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Principal

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CSE

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ECE

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IT

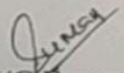
Copy to HODs:
Class room Circulation

Stanley College of Engineering & Technology for Women
Chapel Road, Abids, Hyderabad

Date: 08.02.2021

Examination circular

1. The II Internal Examinations (online) for B.E- VII sem are being scheduled from **15th February 2021**.
2. The concerned faculty is requested to submit the question papers (hard and softcopies - 2 sets in which either of them will be selected) through HOD/dept. exams in-charges to the Exam branch on or before **11th Feb. 2021 before 02.30PM without fail**.
3. The faculty is expected to follow the guidelines of University in setting the question paper.
4. Faculty should mention the new Blooms Taxonomy and CO, PO mapping on the Question paper in the tabular form. No deviation is entertained.
5. The HODs are requested to verify the submission of all subject Question papers on time.
6. The HOD/exam in-charges are requested to submit the list of online invigilators to the exam branch as per the request on or before **11th Feb. 2021 without fail**.


I/C Exams

Principal

Copy to HOD's:

CSE

ECE

EEE

IT

MBA

H&S

Stanley College of Engineering and Technology for Women

B.E - VII Sem - II Internal Examinations (CIE) for the A.Y-2020-2021

08.02.2021

Date / Time	CSE - VII SEM		ECE - VII SEM		EEE - VII SEM		IT - VII SEM	
	9.30-10.30AM	02.30 - 03.30PM	9.30-10.30AM	02.30 - 03.30PM	9.30-10.30AM	02.30 - 03.30PM	9.30-10.30AM	02.30 - 03.30PM
15.02.2021	CC	Dis.Sys	ES	VLSID	PSOC	EDSC	VLSID	FIOT(E-II)
16.02.2021	IS	DM	MWT	IAFM	EMD	FIOT(E-II)	BDA	NS&C
17.02.2021	IOT(E-II)	RSE(E-III)	EMI(PE-II)	R.Prog(OE-II)	RSE(E-III)		WMC	RSE(E-III)
18.02.2021			SE(OE-III)	HVPE				

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LC Exams

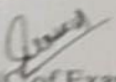
[Signature]
Principal

Stanley College of Engineering & Technology for Women
Chapel Road, Abids, Hyderabad

Date: 08/02/2021

Examination circular

All the B.E VII semester students are informed that, online II - Internal examinations are scheduled from 15th February 2021. The detailed time table will be displayed on notice boards and the students are requested to note the same. Mid exams are conducted online and No Re-test will be conducted for the Absentee students.


I/C of Exams

Principal

Copy to HOD's:

CSE

ECE

EEE

IT

H&S

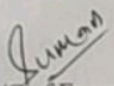
MBA

Stanley College of Engineering & Technology for Women
Chapel Road, Abids, Hyderabad

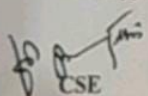
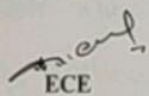

Date: 15.06.2021

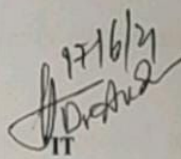
Examination circular

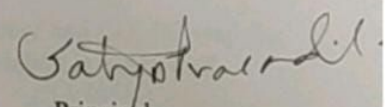
All the B.E IV & VI semester students are informed that, online **I - Internal examinations** are scheduled from **21st - 24th June 2021**. The detailed time table will be displayed on notice boards, and LMS the students are requested to note the same. **Mid exams are conducted online and No Re-test will be conducted for the Absentee students.**

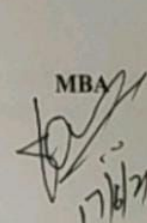
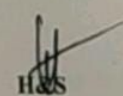

I/C of Exams

Copy to HOD's:

 CSE  ECE  EEE

 17/6/21
IT


Principal

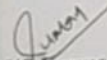
 MBA  H&S
17/6/21

Stanley College of Engineering & Technology for Women
Chapel Road, Abids, Hyderabad

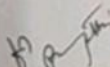
Date: 15.06.2021

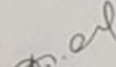
Examination circular

1. The I Internal Examinations (online) for B.E- IV & VI sem are being scheduled from **21st – 24th June, 2021**.
2. The concerned faculty is requested to submit the question papers (softcopies - 2 sets in which either of them will be selected) through HOD/dept. exams in-charges to the Exam branch on or before 18th June, **2021 before 02.30PM without fail in Pdf. Format only.**
3. The faculty is expected to follow the guidelines of University in setting the question paper.
4. Faculty should mention the new Blooms Taxonomy and CO, PO mapping on the Question paper in the tabular form. No deviation is entertained.
5. The HODs are requested to verify the submission of all subject Question papers on time.
6. The HOD/exam in-charges are requested to submit the list of online invigilators to the exam branch as per the request on or before 18th June, 2021 without fail.

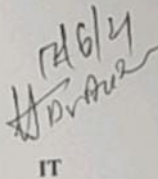

I/C Exams

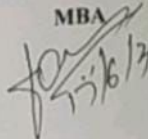
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CSE

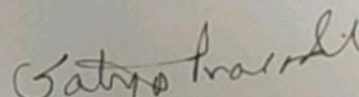

ECE


EEE


IT


MBA


H&S


Principal

Stanley College of Engineering and Technology for Women

B.E -IV and VI Sem - I Internal Examination Time Table (CIE) for the A.Y-2020-2021

Date: 15.06.2021

Date / Time	CSE - IV SEM		ECE - IV SEM		EEE - IV SEM		IT - IV SEM		CME - IV SEM	
	9:30 AM - 10:30 AM	12:30 PM - 1:30 PM	9:30 AM - 10:30 AM	12:30 PM - 1:30 PM	9:30 AM - 10:30 AM	12:30 PM - 1:30 PM	9:30 AM - 10:30 AM	12:30 PM - 1:30 PM	9:30 AM - 10:30 AM	12:30 PM - 1:30 PM
1.06.2021	ETC	FA	S&S	AEC	IC	ETCE	SS	DBS	ETC	FA
2.06.2021	P&S	S&S	EMTL	PDC	F&A	M-III	JAVA	OR	OS	
3.06.2021	JAVA	CO	COA	HVPE	EME	EM-I	DC	COMP	JAVA	CO
4.06.2021	DBMS				DELD	PE	BIOE		DBMS	

Date / Time	CSE - VI SEM		ECE - VI SEM		EEE - VI SEM		IT - VI SEM	
	9:30 AM - 10:30 AM	12:30 PM - 1:30 PM	9:30 AM - 10:30 AM	12:30 PM - 1:30 PM	9:30 AM - 10:30 AM	12:30 PM - 1:30 PM	9:30 AM - 10:30 AM	12:30 PM - 1:30 PM
1.06.2021	CD	CN	DC	DSDV	PS-II	EMI	ES	DM
2.06.2021	DAA	ML	DCCN	EMI	DSPA	UEE	ISM	DAA
3.06.2021	DM	SS&SI	IOT	JAVA	JAVA	DS using R Prog.	DS using R Prog.	DM

Principal

Signature of Principal

Signature of ECE

Signature of CSE

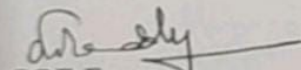
Signature of EEE

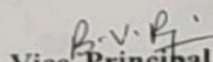
Stanley College of Engineering & Technology for Women
Chapel Road, Abids, Hyderabad

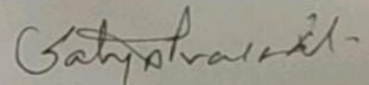
Date: 23.08.2021

Examination circular

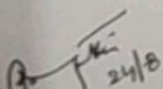
1. The II Internal Examinations (online) for B.E- II, IV, VI sem are being scheduled from **01st to 04th SEP, 2021**.
2. The concerned faculty is requested to submit the question papers (hard and softcopies - 2 sets in which either of them will be selected) through HOD/dept. exams in-charges to the Exam branch on or before 26th Aug. 2021 before 02.30PM without fail in Pdf. format.
3. The faculty is expected to follow the guidelines of University in setting the question paper.
4. Faculty should mention the new Blooms Taxonomy and CO, PO mapping on the Question paper in the tabular form. No deviation is entertained.
5. The HODs are requested to verify the submission of all subject Question papers on time.
6. The HOD/exam in-charges are requested to submit the list of online invigilators to the exam branch as per the request on or before 26th Aug. 2021 without fail.

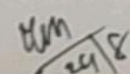

COE Exams

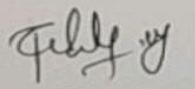

Vice Principal

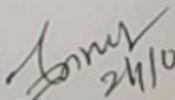

Principal

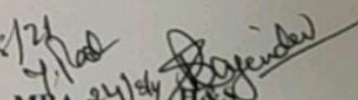
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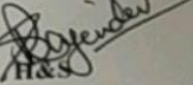

CSE


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MBA

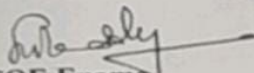

H&S

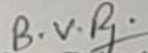
Stanley College of Engineering & Technology for Women
Chapel Road, Abids, Hyderabad

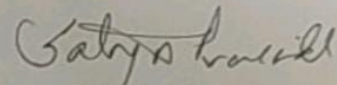
Date: 23.08.2021

Examination circular

All the B.E II, IV & VI semester students are informed that, online **II - Internal examinations** are scheduled on **01st to 04th SEP. 2021**. The detailed time table will be displayed on notice boards and LMS the students are requested to note the same. **Mid exams are conducted online and No Re-test will be Possible for the Absentee students as the marks are to be Upload in OU Portal immediately.**



COE Exams

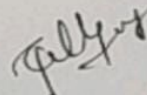

B.V.R.
Vice Principal

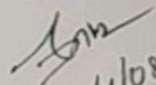

Principal

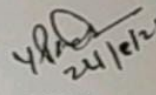
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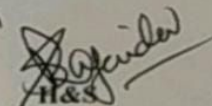

CSE


ECE 24/8


EEE


IT 24/08/21


MBA 24/8/21


H&S

Stanley College of Engineering and Technology for Women
B.E -II, IV, VI Sem - II Internal Examination (CIE) for the A.Y-2020-2021

Date: 23.08.2021

Time	CSE - IV SEM		ECE - IV SEM		EEE - IV SEM		IT - IV SEM		CME - IV SEM	
	9:30 AM - 10:30 AM	12:30 PM - 1:30 PM	9:30 AM - 10:30 AM	12:30 PM - 1:30 PM	9:30 AM - 10:30 AM	12:30 PM - 1:30 PM	9:30 AM - 10:30 AM	12:30 PM - 1:30 PM	9:30 AM - 10:30 AM	12:30 PM - 1:30 PM
2021	ETC	F/A	S&S	AEC	ETCE	F&A	SS	DBS	ETC	F/A
2021	P&S	S&S	EMTL	PDC	M-III	EME	JAVA	OR	OS	
2021	JAVA	CO	COA	HVPE	EM-I	DELD	DC	COMP	JAVA	CO
2021	DRMS				PE		BIOE		DRMS	

Time	CSE - VI SEM		ECE - VI SEM		EEE - VI SEM		IT - VI SEM	
	9:30 AM - 10:30 AM	12:30 PM - 1:30 PM	9:30 AM - 10:30 AM	12:30 PM - 1:30 PM	9:30 AM - 10:30 AM	12:30 PM - 1:30 PM	9:30 AM - 10:30 AM	12:30 PM - 1:30 PM
2021	CD	CN	DC	DSDV	PS-II	EMI	ES	DM
2021	DAA	ML	DCN	EMI	DSPA	UEE	ISM	DAA
2021	DM	SS&SI	IOT	JAVA	JAVA	DS using R Prog.	DS using R Prog.	DMM

B.E II sem.

ATE	9:30 AM - 10:30 AM		12:30 PM - 1:30 PM	
	GROUP A	GROUP B	GROUP A	GROUP B
9.2021	MATHEMATICS II	MATHEMATICS II	ETK	IC
9.2021	CHEMISTRY	PHYSICS	ES	NO EXAM
9.2021	PPS	BEE	ENGLISH (except for AI&DS)	ENGLISH (except for EEE)

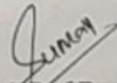
[Signature]
 Vice principal
 ECE
 24/08/21
[Signature]
 Principal
 IT
 24/08/21
[Signature]
 Principal
 EEE
 24/08/21

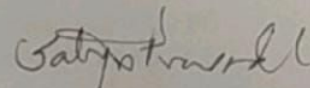
Stanley College of Engineering & Technology for Women
Chapel Road, Abids, Hyderabad

Date: 18.02.2021

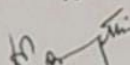
Examination circular

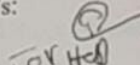
All the B.E III & V semester students are informed that, online **II - Internal examinations** are scheduled from **March 01st 2021**. The detailed time table will be displayed on notice boards and the students are requested to note the same. **Mid exams are conducted online and No Re-test will be conducted for the Absentee students.**

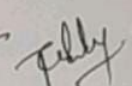

In-charge of Exams

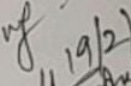

Principal

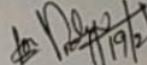
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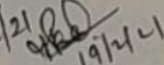

CSE


ECE


EEE


IT


H&S


MBA

Stanley College of Engineering & Technology for Women
Chapel Road, Hyderabad

B.E IV Sem CME II-Mid Examinations-03/09/2021

Computer Organization

SET-1

[Time: 1 Hour]

[Time: 12:30 P M to– 01:30 P.M]

[Max.Marks: 20]

Note: 1) Answer all questions of Part-A

2) Answer any two questions from Part-B

PART-A(6 Marks)

1. What is Average access time. (2)
2. Write about Asynchronous data transfer? (2)
3. Explain what is Boot strap loader? (2)

PART-B(14 Marks)

4. Summarize the operation of DMA controller and its working with a neat sketch. (7)
5. Explain two -way set associative mapping cache memory with an example. (7)
6. What is Cache memory, explain the mechanism of data transfer between cache and main memory. (7)

Q. NO	Q1	Q2	Q3	Q4	Q5	Q6
COs	CO2	CO2	CO3	CO2	CO3	CO3
Learning Levels	Analyze	Create	Understand	Evaluate	Understand	Analyze

Prepared by:

H.Meenal

K.Srilatha

Amtul Sana Amreen

SET - A

**Stanley College of Engineering & Technology for Women
Chapel Road, Hyderabad**

B.E IV-Semester (ETC) II-Mid Examination, Sep 2020-2021

Common to CSE, CME, EEE, Branches

Effective Technical Communication in English (HS201EG)

Time: 1 Hour]

[Marks:20]

- Note:** 1) Answer all questions of Part-A
2) Answer any two questions from Part-B

PART-A

(3X2=6)

1. What is a Project report?
2. What is Operation Manual?
3. Name three important features of a good presentation?

PART-B

(7X2=14)

1. Prepare a user manual on your Smart phone?
2. Write about the different aspects of oral presentation?
3. Fantasy Garments Corporation wants to open a new garment branch in a metropolitan city for all age groups, ranging from formals to casuals. You, as the Manager of the company have to prepare a report on the set- up of the new showroom.

Questions	Q1	Q2	Q3	Q4	Q5	Q6
COs	CO3	CO4	CO5	CO4	CO5	CO3
	Remember and Create	Remember	Create	Remember & Create	Remember	Remember & Create

Set by
Virgilia Richard

Stanley College of Engineering & Technology for Women

Chapel Road, Hyderabad

B.E IV Sem (CME) II-Mid Examinations – 3rd September 2020

OOPS USING JAVA(PC 231 CM)

[Time: 1 Hour]

[Time: 09:30 A. M – 10.30 A.M]

[Max.Marks: 20]

Note: 1) Answer all questions of Part-A

2) Answer any two questions from Part-B

PART-A (6 Marks)

1. What is the AWT class hierarchy? (2)
2. Explain the concept of servlet. (2)
3. Write a program for hash set using utility classes in java. (2)

PART-B (14 Marks)

4. Explain the concept of stream classes in java. Define byte stream classes with diagram. (7)
5. Describe the AWT classes with one example. (7)
6. (a)What is swing Components? Explain in detail with example. (7)
(b) Explain JDBC in detail with one example.

Q. NO	Q1	Q2	Q3	Q4	Q5	Q6
Cos	CO4	CO5	CO3	CO3	CO4	CO4,CO5
Learning Levels	Level 1 Knowledge	Level 1 Knowledge	Level 4 Knowledge	Level 2 Comprehension	Level 1 Knowledge	Level 2 Comprehension

Paper set by

Mrs. M.Swapna, Mrs.Shivani

Stanley College of Engineering & Technology for Women

Chapel Road, Hyderabad

B.E IV Semester (CSE-A/B/C) II-Mid Examinations 4TH Sept 2021

Database Management Systems

[Time: 1 Hour]

[Time:9.30-10.30 P.M]

[Maximum. Marks: 20]

Note: 1) Answer all questions of Part-A

2) Answer any two questions from Part-B

PART-A (6 Marks)

1. What is Thomas Write Rule? (2)
2. Write a short PL/SQL code to demo stored procedures. (2)
3. Write a PL/SQL code to demo Trigger. (2)

PART-B (14 Marks)

4. What is Normalization? Write in brief about 1NF,2NF,3NF Normal forms. Explain with an example 1NF and 2NF. (7)
5. Explain in detail View Serializability. (7)
6. List and explain in detail the problems in executing transactions concurrently. Only list the protocols for concurrency control. (7)

Q. NO	Q1	Q2	Q3	Q4	Q5	Q6
COs	CO1	CO1	CO2	CO1	CO1	CO2
Learning Levels	Level 1 Knowledge	Level 3 Application	Level 3 Application	Level 2,3,4 Comprehension , Application, Analysis	Level 2,3,4 Comprehension , Application, Analysis	Level 2,3,4 Comprehension , Application, Analysis

Paper set by Dr B V Ramana Murthy, Mrs. D Radhika, Mrs Santoshini, Mrs Revathi

Stanley College of Engineering & Technology for Women

Chapel Road, Hyderabad

B.E IV Sem (CSE-A/B/C) II-Mid Examinations – 3rd September 2020

OOPS USING JAVA (PC 231 CS)

[Time: 1 Hour]

[Time: 09:30 A. M – 10.30 A.M]

[Max.Marks: 20]

Note: 1) Answer all questions of Part-A

2) Answer any two questions from Part-B

PART-A (6 Marks)

1. Define an applet. Write the structure of simple applet code. (2)
2. Write a program in java for converting numbers to and from strings using IO packages. (2)
3. List out the wrapper classes? (2)

PART-B (14 Marks)

4. Explain String Tokenizer with an example. (7)
5. Explain the concept of layout managers in AWT components. (7)
6. (a) What is the difference between multithreading and multitasking? Explain the lifecycle of a thread. (7)
- (b) Explain the procedure of event handling with methods.

Q. NO	Q1	Q2	Q3	Q4	Q5	Q6
Cos	CO5	CO4	CO3	CO4	CO5	CO3,CO5
Learning Levels	Level 1 Knowledge	Level 1 Knowledge	Level 4 Knowledge	Level 2 Comprehension	Level 1 Knowledge	Level 2 Comprehension

Paper set by

Mrs. M.Swapna, Mrs.Shivani

Stanley College of Engineering and Technology for women
Chappel Road :: Abids - Hyd.
BE 2nd - II Sem (CSE) II Mid Examinations, Sept, 2021.

Time: 1 hour

Sub: Mathematics-III
(Probability and Statistics)

Max. Marks: 20

Note: Answer ALL questions from Sec. A.
Answer Any Two from Section B.

Section A (6 Marks)

- 1) Students of a class were given an aptitude test. Their marks were found to be normally distributed with mean 60 and Standard deviation 5. What Percentage of Students scored more than 60 marks.
- 2) Fit a straight line to the following data by method of least squares i.e. $y = a + bx$.

x	-2	-1	0	1
y	6	3	2	2

- 3) A fertiliser mixing machine is set to give 12 Kg of nitrate for quintal bag of fertiliser. Ten 100kg bags are examined. The percentage of nitrate per bag are as follows:
11, 14, 13, 12, 13, 12, 13, 14, 11, 12. Is there any reason to believe that the machine is defective (table value of t for 9 dof is 2.262).

Section B (14 Marks)

4. a) Find the mgf of normal distribution.
b) A manufacturer knows from experience

Contd. 2...

Page - 2

- b) Ten participants in a contest are ranked by two judges as follows:

X	1	6	5	10	3	2	4	9	7	8
Y	6	4	9	8	1	2	3	10	5	7

Calculate the rank correlation coefficient.

5. a) Two independent samples of 8 and 7 items respectively had the following values of variables

Sample 1 9 11 13 11 15 9 12 14

Sample 2 10 12 10 14 9 8 10

Is the difference between the means of the sample significant? given $V=13$ $t_{0.05}=2.16$

- b) write two properties of χ^2 distribution.

— x —

Q1	Q2	Q3	Q4		Q5		Q6	
			a	b	a	b	a	b
C03	C04	C05	C03	C03	C04	C04	C05	C05
Evaluating	Analysing	applying	under standing	Analysing	Evaluating	under standing	applying	reason bering

Stanley College of Engineering & Technology for Women
Chapel Road, Hyderabad
B.E. IV Semester (CSE- A,B&C) – Mid-II Examination, 2nd September 2021

Signal and Systems (ES216EC)

[Time: 1hr] Date of Exam: 02-09-2021 [Max. Marks: 20]

Note: 1) Answer all questions of Part-A

2) Answer any two questions from Part-B

PART – A (6 Marks)

1. State the properties of cross correlation [2]
2. Find Fourier transform using properties $x(t) = e^{-2t} \cos(4t)u(t)$ [2]
3. Define Sampling. [2]

PART – B (14 Marks)

4. (a) Solve the Differential Equation using unilateral Laplace Transform $d^2 x(t)/dt^2 + 3 dx(t)/dt + 2 x(t) = 2 u(t)$ with initial conditions $x(0^-) = 1$ & $x'(0^-) = 0$ [4]
 (b) Determine the Z Transform and Plot the ROC for the sequence
 $x[n] = a^n u[n] - b^n u[n]$, $b > a$ [3]
5. (a) What are the Properties of ROC in Laplace transform [3]
 (b) Find the laplace transform of $x(t) = \delta(t) - 4/3 e^{-t} u(t) + 1/3 e^{-2t} u(t)$. [4]
6. (a) Determine the Fourier transform of the signal $x(t) = e^{-at} u(t)$, $a > 0$, plot the Magnitude and Phase Spectrum. [5]
 (b) State and prove parseval's theorem for Fourier transform. [2]

Question	Q1	Q2	Q3	Q4		Q5		Q6	
				a	b	a	b	a	b
Course Outcome No	ES216.4EC	ES216.3EC	ES216.5EC	ES216.5EC	ES216.5EC	ES216.4EC	ES216.4EC	ES216.3EC	ES216.3EC
Bloom's Taxonomy Level	Remembering (BLT-1)	Remembering (BLT-1)	Remembering (BLT-1)	Applying (BLT 3)	Evaluate (BLT 6)	Remembering (BLT-1)	Remembering (BLT-1)	Applying (BLT 3)	Remembering (BLT-1)

Prepared by:

1.Swetha V

2.U.Himavarsha

Stanley College of Engineering & Technology for Women
Chapel Road, Hyderabad
B.E. 3/4 (CSE A,B,C) VI Semester II- Mid Examination, 1st September 2021
Compiler Design
SET-1

Time: 1 Hour] 9:30 AM -10:30AM

Max. Marks: 20

Note: 1) Answer all questions of Part-A

2) Answer any two questions from Part-B

PART – A (6 Marks)

- 1) Define Syntax Directed Translation with an example (2)
- 2) What is Symbol Table (2)
- 3) Write Quadruple notation for $a^* - (b+c)$ (2)

PART – B (14 Marks)

- 4) Explain Type Checking in detail. (7)
- 5) Explain Run-time Environment in detail. (7)
- 6) Explain DAG for $a + (a*(b-c)) + (b-c)*d$ (7)

Paper Set by M.SOWMYA

S.Priya Nandini

.Dept...CSE

QNo	Q1	Q2	Q3	Q4	Q5	Q6
COs	CO3	CO4	CO5	CO3	CO4	CO5
Learning level	level-I	Level-III	Level-I	Level-II	Level-VI	Level-I

SET-1

Stanley College of Engineering & Technology for Women

Chapel Road, Hyderabad

B.E V Semester (CSE-A/B/C) I-Mid Examinations -27TH OCTOBER 2020

SOFTWARE ENGINEERING (PC501CS)

[Time: 1 Hour]

[11.00 A.M-12.00P.M]

[Maximum Marks: 20]

Note: 1) Answer all questions of Part-A

2) Answer any two questions from Part-B

PART-A (6 Marks)

- | | |
|---------------------------------------------------------------|-----|
| 1. Draw a diagram to depict Software Process Assessment. | (2) |
| 2. List Umbrella activities under software process framework. | (2) |
| 3. Write about class diagram with a suitable example. | (2) |

PART-B (14 Marks)

- | | |
|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------|-----|
| 4. Comprehend the different framework activities and different points of emphasis on Agile process models like Extreme Programming and Scrum. | (7) |
| 5. List the phases of requirements gathering and evaluate the role of requirements engineer during each phase. | (7) |
| 6. Categorize approaches for Requirements Modeling. Analyze in detail about Scenario based modeling, class based modeling, behavioral modeling, flow oriented modeling. | (7) |

Q.NO	Q1	Q2	Q3	Q4	Q5	Q6
COs	CO1	CO1	CO2,3	CO1	CO2	CO3
Learning Level	Level1 Knowledge	Level1 Knowledge	Level2 Comprehension	Level4 Analysis	Level6 Evaluation	Level4 Analysis

Stanley College of Engineering & Technology for Women

Chapel Road, Hyderabad

B.E. 3/4 VI Semester, II-Mid Examination, 9th Sept 2021

Design and Analysis of Algorithms

SET-1

Time: 1 Hour]

[Time: 9.30A. M – 10.30A. M]

[Max. Marks: 20

Note: 1) Answer all questions of Part – A.

2) Answer any two questions from Part – B.

PART – A

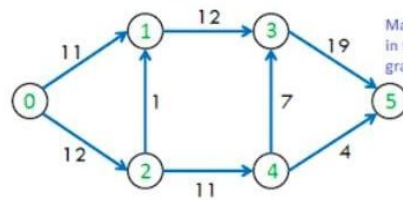
(Marks: 3×2 =6)

- 1) What is dominance rule? (2)
- 2) What is DAG? (2)
- 3) Differentiate P, NP, NP-Hard and NP-Complete. (2)

PART – B

(Marks: 2×7 =14)

- 4) Find the maximum flow for the give graph. (7)



- 5) Explain the types and uses of tries in detail. (7)

- 6) Solve the knapsack problem using backtracking for the following problem:

$P = \{11, 21, 31, 33, 43, 53, 55, 65\}$, $w = \{1, 11, 21, 23, 33, 43, 45, 55\}$, $m = 110$, $n = 8$. (7)

Paper Set by: Mrs.B.G.Prasuna

Dept: CSE

QNo	Q1	Q2	Q3	Q4	Q5	Q6
COs	CO3	CO4	CO5	CO3	CO3	CO4
Learning level	KNOWLEDGE	KNOWLEDGE	ANALYSIS	REMEMBER	UNDERSTAND	ANALYSIS

Set: 2

Stanley College of Engineering & Technology for Women

Chapel Road, Hyderabad

B.E VI SEM (CSE) II-Mid Examinations – 04th Sep, 2021

DATA MINING

[Time: 1 Hour]

[Time: 09.30 A.M – 10.30 A.M]

[Max. Marks: 20]

Note: 1) Answer all questions of Part-A

2) Answer any two questions from Part-B

PART-A (2 x 3 = 6 Marks)

1. Write short note on Bayesian Belief Network (BBN). (2)
2. How K - means algorithm differ from K - medoids? (2)
3. What is Spatial mining? (2)

PART-B (7 x 2 = 14 Marks)

4. Write the algorithm for decision tree induction and explain the construction with example dataset given below. (7)

Outlook	Temperature	Humidity	Windy	Play Golf
Rainy	Hot	High	False	No
Rainy	Hot	High	True	No
Overcast	Hot	High	False	Yes
Sunny	Mild	High	False	Yes
Sunny	Cool	Normal	False	Yes
Sunny	Cool	Normal	True	No
Overcast	Cool	Normal	True	Yes
Rainy	Mild	High	False	No
Rainy	Cool	Normal	False	Yes
Sunny	Mild	Normal	False	Yes
Rainy	Mild	Normal	True	Yes
Overcast	Mild	High	True	Yes
Overcast	Hot	Normal	False	Yes

5. Describe the working of DBSCAN algorithm for clustering (7)
6. Write short notes on any two of the following (3.5 x 2 = 7)
(a) Text Mining. (b) Data mining applications (c) Evaluation of Clustering.

Prepared by:

Dr. K. Vaidehi

Question No	Q1	Q2	Q3	Q4	Q5	Q6
Bloom's Taxonomy Level	Knowledge	Knowledge	Comprehension	Analysis	Knowledge	Comprehension & Analysis

Stanley College of Engineering & Technology for Women
Chapel Road, Hyderabad
B.E. 3/4 (CSE A,B,C) VI Semester II- Mid Examination, 2nd September 2021
Machine Learning
SET-2

Time: 1 Hour] 12:30 PM -1:30 PM

Max. Marks: 20

Note: 1) Answer all questions of Part-A

2) Answer any two questions from Part-B

PART – A (6 Marks)

- 1) Briefly explain Boosting method (2)
- 2) What is feature representation learning? Explain (2)
- 3) What is Reinforcement Learning? Given an example model (2)

PART – B (14 Marks)

- 4) Explain Ada Boost algorithm for binary classification (7)
- 5) Explain Bayesian Learning in detail (7)
- 6) Explain in detail the applications of Deep learning (7)

Paper Set by Dr. C. Kishore Kumar

Dr. P.R.Anisha

Rajashekar Sahstry

.Dept...CSE

QNo	Q1	Q2	Q3	Q4	Q5	Q6
COs	CO3	CO4	CO5	CO3	CO4	CO5
Learning level	level-II	Level-I	Level-II	Level-II	Level-V	Level-I

SET - A

**Stanley College of Engineering & Technology for Women
Chapel Road, Hyderabad**

B.E IV-Semester (ETC) II-Mid Examination, Sep 2020-2021

Common to CSE, CME, EEE, Branches

Effective Technical Communication in English (HS201EG)

Time: 1 Hour]

[Marks:20]

- Note:** 1) Answer all questions of Part-A
2) Answer any two questions from Part-B

PART-A

(3X2=6)

1. What is a Project report?
2. What is Operation Manual?
3. Name three important features of a good presentation?

PART-B

(7X2=14)

1. Prepare a user manual on your Smart phone?
2. Write about the different aspects of oral presentation?
3. Fantasy Garments Corporation wants to open a new garment branch in a metropolitan city for all age groups, ranging from formals to casuals. You, as the Manager of the company have to prepare a report on the set- up of the new showroom.

Questions	Q1	Q2	Q3	Q4	Q5	Q6
COs	CO3	CO4	CO5	CO4	CO5	CO3
	Remember and Create	Remember	Create	Remember & Create	Remember	Remember & Create

Set by
Virgilia Richard

Stanley College of Engineering & Technology for Women

Chapel Road, Hyderabad

B.E IV Semester (CME-A/B/C) II-Mid Examinations 4TH Sept 2021

Database Management Systems

[Time: 1 Hour]

[Time:9.30-10.30 P.M]

[Maximum. Marks: 20]

Note: 1) Answer all questions of Part-A

2) Answer any two questions from Part-B

PART-A (6 Marks)

1. What is Thomas Write Rule? (2)
2. Write a short PL/SQL code to demo stored procedures. (2)
3. Write a PL/SQL code to demo Trigger. (2)

PART-B (14 Marks)

4. What is Normalization? Write in brief about 1NF,2NF,3NF Normal forms. Explain with an example 1NF and 2NF. (7)
5. Explain in detail View Serializabilty. (7)
6. List and explain in detail the problems in executing transactions concurrently. Only list the protocols for concurrency control. (7)

Q. NO	Q1	Q2	Q3	Q4	Q5	Q6
COs	CO1	CO1	CO2	CO1	CO1	CO2
Learning Levels	Level 1 Knowledge	Level 3 Application	Level 3 Application	Level 2,3,4 Comprehension , Application, Analysis	Level 2,3,4 Comprehension , Application, Analysis	Level 2,3,4 Comprehension , Application, Analysis

Paper set by Dr B V Ramana Murthy, Mrs. D Radhika, Mrs Santoshini, Mrs Revathi

Stanley College of Engineering & Technology for Women
Chapel Road, Hyderabad

B.E IV Sem CME II-Mid Examinations-03/09/2021

Computer Organization

SET-1

[Time: 1 Hour]

[Time: 12:30 P M to– 01:30 P.M]

[Max.Marks: 20]

Note: 1) Answer all questions of Part-A

2) Answer any two questions from Part-B

PART-A(6 Marks)

1. What is Average access time. (2)
2. Write about Asynchronous data transfer? (2)
3. Explain what is Boot strap loader? (2)

PART-B(14 Marks)

4. Summarize the operation of DMA controller and its working with a neat sketch. (7)
5. Explain two -way set associative mapping cache memory with an example. (7)
6. What is Cache memory, explain the mechanism of data transfer between cache and main memory. (7)

Q. NO	Q1	Q2	Q3	Q4	Q5	Q6
COs	CO2	CO2	CO3	CO2	CO3	CO3
Learning Levels	Analyze	Create	Understand	Evaluate	Understand	Analyze

Prepared by:

H.Meenal

K.Srilatha

Amtul Sana Amreen

Dr. L.Nirmala Devi
Associate Professor and
Chair Board of Studies in ECE
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*Department of Electronics and
Communication Engineering
University College of Engineering*
OSMANIA UNIVERSITY
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Fax: 040-27095179*

No: /ECE/OUCE/BoS/2020

Date: 3rd Feb, 2020

Dear Sir/Madam,

Sub: Board of Studies Meeting-Reg

A meeting of Board of Studies in Electronics and Communication Engineering is scheduled on 5th February, 2020 at 10:00 AM in the VLSI LAB of the Department to discuss the detailed revised syllabus of the BE program.

In this connection, I request all the HoD s of the affiliated Colleges make it convenient to attend the meeting.

Thanking you,

Yours Sincerely,

(Dr. L.Nirmala Devi)
Chair BOS in ECE (A)
Osmania University, Hyderabad

Dr. L.Nirmala Devi
Associate Professor
Chair Board of Studies in ECE
Director CHW OU
Osmania University
nagilitkp@yahoo.co.in
Mobile: +91-9949513490



*Department of Electronics and
Communication Engineering*
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Fax: 040-27095179

No: 03 /ECE/OUCE/BoS/2020

Date: 23rd October 2020

To
The Members of BOS in ECE and special invitees
Osmania University, Hyderabad-07

Dear Sir/Madam,

Sub: Board of Studies Meeting-Reg.,

A meeting of Board of Studies in Electronics and Communication Engineering is scheduled on 28th October 2020 at 2:00 PM, online mode

Agenda of the Meeting:

1. Review and finalization of scheme of B.E Program for the academic year 2020-2024
2. And any other matter with the permission of the Chair.

In this connection, I request you to make it convenient to attend the meeting.

Thanking you,

Yours Sincerely,

(Dr. L.Nirmala Devi)
Chair BOS in ECE
University College of Engineering
Osmania University, Hyderabad

Minutes of the Meeting held on 5/2/2020 at Osmania University, Hyderabad

2019-2020 (R-19)

III Semester

1. **PTSP**
 - a. Unit-3: Multivariant is removed and Two random variables is replaced
 - b. Unit-5: Case study is removed
2. **Digital Electronics**
 - a. Unit-2: Concepts of adders and subtractors are included
 - b. Unit-4: Verilog Code is removed as they learn in DSD with Verilog HDL
3. **Electronic Devices**
 - a. Title has been modified to Electronic Devices and Circuits
 - b. Unit-1: Title for the unit is changed to Basics of Semiconductors, Hall effect is included and spelling are corrected
 - c. Unit-5: Fabrication Process is removed
 - d. Analysis concepts are removed from MOSFETs unit and placed in JFETs, unit, before which low frequency small signal analysis is placed
 - e. Suggested reading book : 4th book is shifted to 1st.
4. **Network Theory**
 - a. Pi is replaced with symbol.
5. Electronic Devices Lab and Electronic Workshop Lab has no corrections.
6. Credits for PTSP, DE,ED, and NT are increased by 1 (3L + 1T) where the semester credits are 24 Credits

IV Semester

Summer Internship is removed and placed in VI Semester

1. **Signals and Systems**
 - a. Syllabus need to be revised and this will be submitted by Methodist College of Engineering and Technology
2. **Analog Electronics and Circuits**
 - a. No Corrections
3. **Electromagnetic Theory and Transmission Lines:**
 - a. Unit Titles are included
 - b. Unit-3: Applications are removed ,Average and complex power, Poynting theorem, Surface impedance has been included
 - c. Unit-4: Title need to be checked, Types of transmission line are replaced with Two wire transmission lines.
 - d. Unit-5: Title need to be checked.

4. **Pulse and Digital Circuits**
 - a. Title is modified.
 - b. Unit 4 should be having the concept of sampling gates
 - c. Unit 4 and unit 5 should be combined and should be made as unit 5
5. **Computer Organization and Architecture**
 - a. No Corrections
6. **Analog Electronics and Circuit Lab**
 - a. No Corrections
7. **Pulse and Digital Circuits**
 - a. PDC Experiments are added and Sampling gates are included and LICA Experiments are removed.
8. Credits for S&S, AEC, EMTL, are increased by 1 (3L + 1T) where the semester credits are 20 Credits

2018-2019 (R-18)

V Semester

1. DSD with HDL is swapped with AWP which is in VI Semester
2. Summer Internship is swapped with Mini Project which is in VI semester
3. **Analog Communication**
 - a. Unit 1: Titles are included.
 - b. Conventional word is removed and COSTAS Receiver is included
 - c. In Unit 2 and 3, the word scheme is removed
 - d. Swap 3rd and 4th units
 - e. Unit 5 is Titled as Noise
 - f. Titles will be finalized by Dr. Nirmala Devi(OU)
4. **Digital Signal Processing**
 - a. Unit 2 is Titled as IIR filters and Unit 3 is Titled as FIR filters
 - b. Realization of IIR filters are included in Unit 2
5. **Automatic Control Systems**
 - a. Unit-1: Add mathematical model of Mechanical systems and their conversions into electrical systems
 - b. Unit-3: Compensation techniques are included.
6. **Antenna and wave propagation**
 - a. Unit-1: Title is changed, concepts are swapped internally within the unit
 - b. Unit 2,3,4, and 5 need to be finalized
7. **Microprocessor and Microcontrollers**
 - a. Unit 4: Title is included as 8051 Timer and Interrupts
 - b. Unit 5: Title is included as 8051 Interfacing

8. **General Sensitization**
 - a. No Changes
9. **Systems & Signal Processing Lab**
 - a. Part A Introduction to MAT Lab and Signal Generation is included
 - b. Part B Introduction to DSP Processors
 - c. Experiment Statements need to be modified which will be given by MJCET.

VI Semester

1. In this semester VLSI Design is shifted to VII Semester and this slot is replaced with Electronic Instrumentation
2. **Digital Communication**
 - a. Check with Autonomous college Syllabus
3. **Electronic Instrumentation**
 - a. This syllabus will be given by MJCET
4. **Data communication and Computer Networks**
 - a. This syllabus is proposed by Stanley College and is accepted by all the members.
5. **Communication Lab**
 - a. **Part A**
 - i. Exp 1 Title changed to AM Modulation and Demodulation
 - ii. Exp 2 Title changed to FM Modulation and Demodulation
 - iii. Exp 7 is removed (Analog & Pulse modulation techniques using MAT Lab)
 - b. **Part B**
 - i. Exp 1 Title changed to PCM Modulation and Demodulation
 - ii. Exp2 Title changed to Channel Coding and Decoding
 - iii. Exp 6 is removed
6. **DCCN Lab**
 - a. Proposed by Stanley College and Experiments were reduced to 12 No's. It is commented that the Statements are like question paper, which should be modified and should be with UG Level.
7. **Open Elective-I**

The following subjects are offered which follows the previous syllabus only

 - a. Principles of Electronic communications
 - b. DSD using Verilog HDL
8. **Professional Electives-I**

The following subjects are finalized and the detailed syllabus will be given by MJCET

 - a. Image and Video Processing
 - b. Advanced Microcontrollers
 - c. Optical Communications
 - d. IOT Sensors



Maturi Venkata Subba Rao (MVSRR) Engineering College,
Nadergul, Hyderabad - 501 510.

EXAMINATION SECTION

LT.No. MVSREC/Pract. Exams/BE/2021 V Semester

Dt: 02-03-2021.

To
Mrs. C.V. Keerthi Latha, Asst. Prof.
ECE Dept., Stanley College of Engg.
Hyderabad.
Email ID: dkceerthilatha@stanley.edu.in
Ph. No: 9246288398

Dear Sir,

Subj: M.V.S.R. Engg. College - University Practical Examinations.
Appointment of External Examiner - Communication of
Exam Schedule - Request for conducting examinations - Reg.

I am pleased to inform you that you have been appointed as External Examiner by the Additional Controller of Examinations (Confidential Section), Osmania University, Hyderabad - 07, to conduct the Practical Examinations in the subject Systems & Signal Processing Lab (PC 551 EC) for B.E. (ECE) V Semester, Section - C, on 23rd & 24th (Tuesday & Wednesday) March, 2021.

I request you to conduct the said examination as per the schedule. A line in confirmation is very much appreciated in this regard.

Thanking you,

Yours faithfully,

Chief Superintendent
B.E. O.U. Practical Examinations
M.V.S.R. Engineering College

C.C. to:

- 1) Internal Examiner: Mrs. KVBL Deepthi, Asst. Professor,
Email ID: deepthi.kvbl.ec@mvsrc.edu.in, Ph. No. 9704093064
- 2) C.O.E.O.U.
- 3) Copy to file
- 4) Exam branch.

Chief Superintendent
M.V.S.R. ENGINEERING COLLEGE
NADERGUL (P.O.), HYDERABAD

Maturi Venukumar Reddy Engineering College, Nanded, Maharashtra - 500 510
 Time table of Osmania University Practical Examination for B.E. 1st Sem. (ECE) - March, 2021
 ELECTRONICS AND COMMUNICATIONS ENGINEERING

Batch No	Roll Number	Strength
C1	2450-19-735-020 to 2450-19-735-032	12
C2	2450-19-735-033 to 2450-19-735-045	12
C3	2450-19-735-046 to 2450-19-735-058	12
C4	2450-19-735-059 to 2450-19-735-071	12
C5	2450-19-735-072 to 2450-19-735-084	12
C6	2450-19-735-085 to 2450-19-735-097 2450-19-735-098 to 2450-19-735-110	12 + 12 = 24
C7	2450-19-735-111 to 2450-19-735-123	12
C8	2450-19-735-124 to 2450-19-735-136	12
C9	2450-19-735-137 to 2450-19-735-149	12
C10	2450-19-735-150 to 2450-19-735-162	12
C11	2450-19-735-163 to 2450-19-735-175	12
C12	2450-19-735-176 to 2450-19-735-188 2450-19-735-189 to 2450-19-735-201	12 + 12 = 24
C13	2450-19-735-202 to 2450-19-735-214	12
C14	2450-19-735-215 to 2450-19-735-227	12
C15	2450-19-735-228 to 2450-19-735-240	12
C16	2450-19-735-241 to 2450-19-735-253 2450-19-735-254 to 2450-19-735-266	12 + 12 = 24
C17	2450-19-735-267 to 2450-19-735-279	12
C18	2450-19-735-280 to 2450-19-735-292 2450-19-735-293 to 2450-19-735-305	12 + 12 = 24

Date	Systems & Signal Processing			Microprocessor and Microcontroller Lab		
	8:00 AM - 11:00 AM	11:00 AM - 2:00 PM	2:00 PM - 5:00 PM	8:00 AM - 11:00 AM	11:00 AM - 2:00 PM	2:00 PM - 5:00 PM
18-03-2021 (Thursday)	C1	C2	C3	C13	C14	C15
19-03-2021 (Friday)	C4	C5	C6	C16	C17	C18
20-03-2021 (Saturday)	C7	C8	C9	C1	C2	C3
21-03-2021 (Monday)	C10	C11	C12	C4	C5	C6
22-03-2021 (Tuesday)	C13	C14	C15	C7	C8	C9
23-03-2021 (Wednesday)	C16	C17	C18	C10	C11	C12

OU Practical exam Nelson

Dept. Exams

Head, ECE

Chief Supervisor

W. S. R. ENGINEERING COLLEGE
 Nanded, Maharashtra

Stanley College of Engineering & Technology for Women
Chapel Road, Abids, Hyderabad

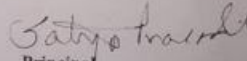
Date: 15.06.2021

Examination circular

1. The 1 Internal Examinations (online) for B.E- IV & VI sem are being scheduled from **21st – 24th June, 2021**.
2. The concerned faculty is requested to submit the question papers (softcopies - 2 sets in which either of them will be selected) through HOD/dept. exams in-charges to the Exam branch on or before 18th June, **2021 before 02.30PM without fail in Pdf. Format only.**
3. The faculty is expected to follow the guidelines of University in setting the question paper.
4. Faculty should mention the new Blooms Taxonomy and CO, PO mapping on the Question paper in the tabular form. No deviation is entertained.
5. The HODs are requested to verify the submission of all subject Question papers on time.
6. The HOD/exam in-charges are requested to submit the list of online invigilators to the exam branch as per the request on or before 18th June, 2021 without fail.


I/C Exams

Copy to HOD's:


Principal

CSE

ECE

EEE

IT

MBA


H&S

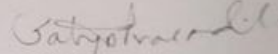
Stanley College of Engineering & Technology for Women
Chapel Road, Abids, Hyderabad

Date: 15.06.2021

Examination circular

All the B.E IV & VI semester students are informed that, online **I - Internal examinations** are scheduled from **21st - 24th June 2021**. The detailed time table will be displayed on notice boards, and LMS the students are requested to note the same. **Mid exams are conducted online and No Re-test will be conducted for the Absentee students.**


L/C of Exams
Copy to HOD's:


Principal

CSE ECE EEE IT MBA H&S

Stanley College of Engineering & Technology for Women

B.E IV- Semester(EEE) , I-Mid Examination–24th June, 2021

Power Electronics (SET-1)

[Time: 1 Hour]

[Time: 12.30 P.M – 1.30 P.M]

[Max. Marks: 20]

Note: 1) Answer all questions of Part-A

2) Answer any two questions from Part-B

PART-A (6 Marks)

1. Define latch up in IGBT? (2)
2. Why it is necessary to use fast recovery diodes for high speed application? (2)
3. Define Duty ratio? (2)

PART-B (14 Marks)

- a) A step up chopper has input voltage of 220V and output voltage of 660V. If the non conducting time of thyristor chopper is 100microseconds.compute the pulse width of output voltage. In case of pulse width is halved for constant frequency operation.find the new output voltage (4)
- b) Explain the working of class C commutation circuit with necessary diagram? (3)
5. a) Describe the switching characteristics of power MOSFET (4)
- b) Explain the operation of single phase full wave AC voltage controller with R load? (3)
6. a) Describe the different modes of operation of a thyristor with the help of its static V-I characteristics. (4)
- b) Explain the working of R triggering circuit of SCR? (3)

CO Mapping and Blooms Taxonomy:

Question	Q1	Q2	Q3	Q4	Q5	Q6
Course Outcome	PC233EE EE 1	PC233EE EE 1	PC233EE EE 3	PC233EE EE 3	PC233EE EE 1	PC233EE EE 1
Bloom's Taxonomy	Remember	Remember	Remember	Analyze knowledge	Knowledge, Remember	Remember, Remember

Paper set by: HEENA PARVEEN

Stanley College of Engineering & Technology for Women

B.E IV- Semester(EEE) , I-Mid Examination-24th June, 2021

Power Electronics (SET-2)

[Time: 1 Hour]

[Time: 12.30 P.M – 1.30 P.M]

[Max. Marks: 20]

Note: 1) Answer all questions of Part-A

2) Answer any two questions from Part-B

PART-A (6 Marks)

1. Write the output voltage equation for different types of choppers? (2)
2. Define " firing angle"? (2)
3. Define the following terms for diodes: (2)
a) Softness factor b) Peak Inverse Voltage

PART-B (14 Marks)

4. a) Explain the working with neat structure of an n-channel power MOSFET and plot its output characteristics (5)
b) Give constructional differences between MOSFET and BJT. (2)
5. a) Describe the switching characteristics of SCR? (5)
b) What are the different types of power diodes? (2)
6. a) Describe the working of buck converter with neat diagram. also derive output equation (5)
b) A step up chopper has output voltage of 2 times of input voltage. For a chopper frequency of 2000hz. Determine off period of the gate signals (2)

***CO Mapping and Blooms Taxonomy:

Question	Q1	Q2	Q3	Q4	Q5	Q6
Course Outcome	PC233EE EE.3	PC233EE EE.1	PC233EE EE.1	PC233EE EE.1	PC233EE EE.1	PC233EE EE.3
Bloom's Taxonomy	Remember	Understand	Understand	Analyze Understand	Remember, Understand	Analyze ,Analyze

Paper set by: HEENA PARVEEN

Assignment-1

Unit-1

1. Give constructional differences between MOSFET and IGBT.
2. What are the different types of power diodes? State the limitations of Schottky diodes
3. Explain the working with neat structure of an n-channel power MOSFET and plot its output characteristics
4. Draw and explain two transistor analogy of SCR.
5. Define the following terms for diodes:
 - a) Softness factor
 - b) Peak Inverse Voltage
6. In R triggering circuit why the firing angle is limited to 90° .
7. Describe the switching characteristics of power MOSFET
8. Describe the different modes of operation of a thyristor with the help of its static V-I characteristics.
9. Draw a circuit diagram illustrating the protection of both anode and gate circuits of an SCR. Describe briefly the function of various components used.
10. Define the following terms for diode
 - a) Reverse recovery time
 - b) reverse recovery current
11. List the different types of commutation technique?
12. explain different types of triggering circuit for scr?
13. Explain different triggering methods of scr?
14. Describe the switching characteristics of power MOSFET
15. Explain different commutation circuits of scr?
16. Give constructional differences between MOSFET and BJT.
17. Why it is necessary to use fast recovery diodes for high speed application?
18. Write advantages of disadvantages of BJT?
19. Define latch up in IGBT and secondary breakdown in BJT?
20. Describe the switching characteristics of SCR?

Assignment-2

1. A step up chopper has output voltage of 2 to 4 times of input voltage. For a chopper frequency of 2000Hz. Determine range of off periods of the gate signals
2. What is inverter? List few industrial applications of inverter
3. A single phase full wave ac voltage controller feeds a load of $R = 20$ with an input voltage of 230 V, 50Hz. Find the RMS value of output voltage if firing angle is 45.
4. Describe the working of buck converter with neat diagram, also derive output equation
5. Explain time ratio control of chopper
6. Describe the working of single phase half bridge inverter with R load
7. For a buck converter A DC voltage source is 230V a load resistance is 10 and takes a voltage drop of 2 volt across chopper when it is on. For a duty cycle of 0.4 calculate average and RMS value of output voltage.
8. What is the purpose of connecting diodes in antiparallel with switches in inverter circuits
9. A step up chopper has input voltage of 220V and output voltage of 660V. If the non conducting time of thyristor chopper is 100microseconds. compute the pulse width of output voltage. In case of pulse width is halved for constant frequency operation, find the new output voltage
10. A single phase full bridge inverter feed power to a load of $R=10$ and $L=0.04$ from a 400V dc source. if the inverter operates at a frequency of 50Hz. determine the power delivered to the load given $T_{on}=0.6$ of Total time period
11. A simple chopper is operating at a frequency of 2kHz from a 100V DC to supply a load resistance of 10ohm. The load time constant of 5msec. if average load voltage is 59 volts. Find T_{on} and average load current
12. Explain sinusoidal pulse width modulation
13. What is meant by pulse width modulation write it's advantages
14. Explain the operation of single phase AC voltage controller with R and RL load
15. Describe the operation of boost converter with neat diagram also derive output equation.

Stanley College of Engineering & Technology for Women
B.E IV- Semester(EEE) , I-Mid Examination-24th JUNE, 2021

Power Electronics

QUIZ PAPER

[Time: 10 Minutes]

[Time: 2.30 A.M – 2.40 A.M]

[Max. Marks: 5]

1. Which of the following diodes uses a metal-semiconductor junction?
 a) General purpose diodes b) Fast recovery diodes
 c) Schottky diode
2. Which of the following devices does not belong to the transistor family?
 a) IGBT b) MOSFET c) SCR d) BJT
3. The forward current gain α is given by
 a) I_C/I_B b) I_C/I_E c) I_E/I_C d) I_E/I_B
4. BJT is a _____ device.
 a) two terminal, bipolar, voltage controlled b) two terminal, unipolar, current controlled
 c) three terminal, unipolar, voltage controlled d) three terminal, bipolar, current controlled
5. di/dt protection is provided to the thyristor by
 a) connecting an inductor in parallel across the scr
 b) connecting an inductor in series with the scr
 c) connecting an inductor in parallel across the gate terminal
 d) connecting an inductor in series with the gate
6. The controlling parameter in IGBT is the
 a) I_G b) V_{GE} c) I_C d) V_{CE}
7. In step down chopper using pulse width modulation $T_{on}=3\text{msec}$ and $T_{off}=1\text{msec}$. The chopping frequency is
 (a) 333.33 (b) 250. (c) 500. (d) 1000
8. A chopper converts
 (a) Fixed DC to variable DC
 (b) Fixed AC to variable AC
9. Duty ratio
 (a) Is the ratio between T_{on} and T
 (b) Is the ratio between T and T_{on}
 (c) Is the ratio between T_{off} and T
10. In Step down chopper output voltage is.....
 (a) less than input voltage (b) equal to input voltage (c) greater than input voltage

*****CO Mapping**

Q.1	Q.2	Q.3	Q.4	Q.5	Q.6	Q.7	Q.8	Q.9	Q.10
CO1	CO1	CO1	CO1	CO1	CO3	CO3	CO3	CO3	CO3

AI MANAC for the Academic year 2020-2021
B.E.(All Branches) I & II – Semesters (*AIU TE Model Curriculum*)
 (First Year)
 (For all Engineering Colleges Affiliated to Osmania University)

L 100 4
 6/10 - 2021

I-Semester

Induction Programme (2 Weeks) in Online	01.12.2020 to 19.12.2020
Commencement of Class work in offline	21.12.2020
CIE (Internal Test) - I	08.02.2021 to 10.02.2021
Display of CIE-I Marks	01.03.2021
CIE (Internal Test) - II	25.03.2021 to 27.03.2021
Last Date of Instruction	03.04.2021
Display of Total Sessional Marks	12.04.2021
Submission of Sessional Marks & Attendance to O.U Exam Branch	13.04.2021
Preparation and Practical Examinations	05.04.2021 to 17.04.2021
Commencement of Theory Examinations (SEE)	19.04.2021

II-Semester

Summer vacation for Faculty & Commencement of Instruction in Online (Theory)	10.05.2021 to 19.06.2021
Class work for Practicals in offline	21.06.2021 to 26.06.2021
CIE (Internal Test) - I	28.06.2021 to 30.06.2021
Display of CIE-I Marks on or before	12.07.2021
CIE (Internal Test) - II	19.08.2021 to 21.08.2021
Last Date of Instruction	28.08.2021
Display of Total Sessional Marks	06.09.2021
Submission of Sessional Marks & Attendance to O.U Exam Branch	07.09.2021
Preparation and Practical Examinations	30.08.2021 to 12.09.2021
Commencement of Theory Examinations	13.09.2021
Commencement of Next Academic Year 2021-2022	04.10.2021

- (1) Principals of Affiliated Colleges may accord permission to Teaching Staff to avail Summer Vacation & may have to attend examinations related work during vacation. Moreover, they will have to conduct Online classes during vacation.
- (2) In case of any public holiday / unscheduled holiday on the day of class test, Principals may reschedule the same immediately on the next working day after intimation to Dean's Office, UCE, O.U.
- (3) In view of extended lockdown for offline instructions, the faculty can compensate the lost classwork by taking extra classes on Saturdays including Second Saturdays.

Dean, Faculty of Engineering, O.U.

Stanley College of Engineering & Technology for Women
Department of Information Technology
AY:20-21

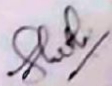
Department Academic Calendar for III, V (AICTE Model) & VII(CBCS Model)

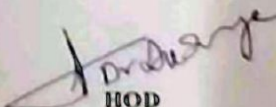
III, V Semester

Date	Activity
01-09-2020	Commencement of Instructions
27-10-2020 to 30-10-2020	CIE-I
31-10-2020	Display of CIE-I Marks
04-01-2021	Commencement of Instructions
01-03-2021 to 04-03-2021	CIE-II
12-03-2021	Last Date of Instructions
22-03-2021	Display of Total Sessional marks
13-03-2021 to 27-03-2021	Preparation and Practical Examination
20-03-2021	Submission of attendance to O.U Exam Branch
29-03-2021	Commencement of Theory Examinations

VII Semester

Date	Activity
01-09-2020	Commencement of Instructions
27-10-2020 to 28-10-2020	CIE-I
31-10-2020	Display of CIE-I Marks
04-01-2021	Commencement of Instructions
15-02-2021 to 18-02-2021	CIE-II
20-02-2021	Last Date of Instructions
22-02-2021	Display of Total Sessional marks
22-02-2021 to 06-03-2021	Preparation and Practical Examination
27-02-2021	Submission of attendance to O.U Exam Branch
08-03-2021	Commencement of Theory Examinations


I/C Faculty


HOD


Stanley College of Engineering & Technology for Women
Department of Information Technology
AY: 20-21
Department Academic Calendar B.E IV,VI (AICTE Model)VIII(CBCS Model)

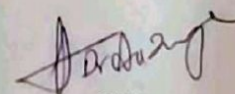
IV & VI Semester

Date	Activity
26-04-2021	Commencement of Instruction
10-05-2021 to 19-06-2021	Summer vacation
21-06-2021 to 26-06-2021	CIE-I
30-06-2021	Display of CIE- I
01-09-2021 to 04-09-2021	CIE-II
10-09-2021	Last Date of Instructions
30-09-2021	Commencement of Theory Examinations
07-10-2021	Display of total sessional Marks
09-10-2021	Submission of attendance to O.U Exam Branch
22-11-2021 to 5-11-2021	Practical examination
08-11-2021	Commencement of Next Academic Year 2021-2022

VIII Semester

Date	Activity
22-03-2021	Commencement of Instruction
15-04-2021 to 17-04-2021	CIE-I
22-04-2021	Display of CIE-I
03-05-2021 to 05-05-2021	CIE-II
07-05-2021	Last date of Instruction
15-05-2021	Display of total sessional marks
07-05-2021 to 23-05-2021	Preparation for Practical Examinations
15-05-2021	Submission of attendance to O.U Exam Branch
17-05-2021	Submission of CIE Marks to OU
21-06-2021 and 22-06-2021	Project Work VIVA VOCE
05-07-2021 TO 14-07-2021	Commencement of Theory Examinations


I/C Faculty


HOD

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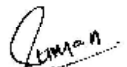
STANLEY COLLEGE OF ENGINEERING AND TECHNOLOGY FOR WOMEN

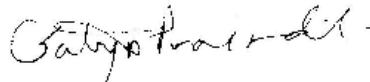
Chapel Road, Abids

CIRCULAR

Date: 20.04.19

1. The II Internal examinations for MBA II & IV Semester is being scheduled on 25th & 26th April, 2019. The detailed Time table is enclosed with this circular.
2. The concerned faculty is requested to give two sets of Question Papers per subject by 22nd April, 2019 through dept. I/C Exams.
3. The departmental exams- in- charge is requested to give the names of invigilators as per the Time table enclosed


I/C Exams


Principal

Copy to HOD


MBA






20/4/19